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# PRELIMINARY

## SCHEMATIC

## ELECTRICAL CHARACTERISTICS @ 25°C

TURNS RATIO TP1 TP2 TP3 TP4	1CT : 1CT ±2% 1CT : 1CT ±2% 1CT : 1CT ±2% 1CT : 1CT ±2%
OCL @ 100kHz/100mVRMS 8mA DC BIAS	(0−70°C) 350µH MIN.
INS. LOSS  0.1MHz TO 1MHz  1MHz TO 65MHz  65MHz TO 100MHz  100MHz TO 125MHz	-1.1 dB MAX -0.5 dB MAX -0.8 dB MAX -1.2 dB MAX
RET. LOSS (MIN) @ 100 OHM 0.5MHz-40MHz	S +/-15%  -18 dB

R 40MHz-100MHz  $-12+20L\Box G(f/80MHz) dB$ CM TO CM REJ

-30 dB MIN

CM TO DM REJ

100kHz - 100MHz

-35 dB MIN 100kHz - 100MHz

HIPOT (Isolation Voltage): 1500 Vrms

100% OF PRODUCTION TESTED TO COMPLY WITH IEEE 802.3 ISOLATION REQUIREMENTS.

SCHEMATIC							
	RJ45						
TRD1+ 11 - 1CT : 1CT  TRCT1 12 - 74.7PF	1 TRP1+						
TRD1- 10 •	2 TRP1-						
TRD2+ 4 • 1CT : 1CT  TRCT2 6 • 17-7PF	3 TRP2+						
TRD2- 5 • 3  \$	6 TRP2-						
TRD3+ 3 1 1CT : 1CT TRCT3 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	4 TRP3+						
TRD3- 2 • 3  \(\xi \)	5 TRP3-						
TRD4+ 8 1CT : 1CT TRCT4 7 1CT TA.7PF	7 TRP4+						
TRD4- 9 •	8 TRP4-						
4X 75 □HMS \$ \$ \$ \$							
1000pF 2kV $\frac{\bot}{T}$							
SHIELD 7777							
O / DDAWING NO							

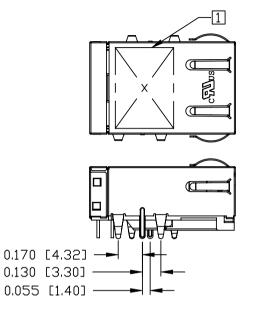
ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STAND	ARD DIM.	[ ] METRIC DIM. AS	REFERENCE	Τ.
CHOW WANCHUNG	2012-01-18		L835-1X1T-43	TOL.	IN INCH	UNIT : INCH [mm]	REV. : PB	11
DRAWN BY	DATE	(NanoJack)	FILE NAME	.X		SCALE: N/A	SIZE : A4	٦,
LEE WEIDE	2012-01-18	PATENTED	L835-1X1T-43_PB.DWG	.XXX			PAGE: 2	1
7,0000(0)004444		PT 1 3 4 1	1					

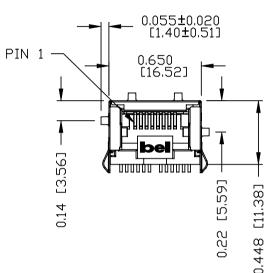
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## MECHANICAL SPECIFICATION









NOTES:

PLASTIC HOUSING: THERMOPLASTIC PBT

FLAMMABILITY RATING UL 94V-0

CONTACTS: 30 MICRO-INCH HARD GOLD PLATING OR EQUIVALENT

DUTPUT PINS: TIN-COATED COPPER WIRE, DIA 0.018 INCH.

METAL SHIELD: NICKEL PLATED ON COPPER ALLOY.

(ALL GROUND LEADS ARE SOLDER DIPPED)

JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS, PART 68 SUBPART F.

- [] MARK PART WITH MFG. LOGO, MFG. NAME, PART NUMBER, AND DATE CODE.
- 2. MARK PART WITH PATENTED.
- 3. THE PRODUCT IS ROHS COMPLIANT.

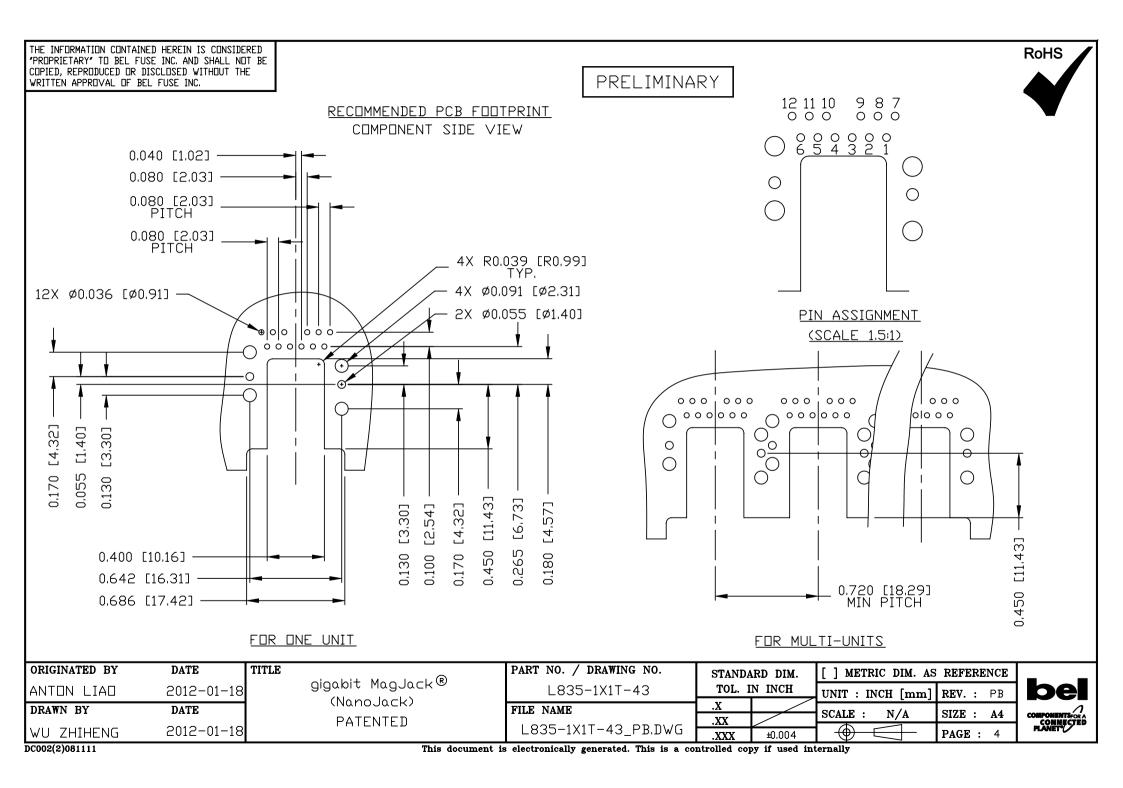
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0.130 [3.30] 0.130 [3.30] 0.130 [3.30] 0.130 [3.30] 0.130 [3.30] 0.130 [3.30] 0.130 [3.30] 0.130 [3.30]	1.026 [26.06]	0.125 [3.17] SIGNAL LEADS ——

4. THE PRODUCT IS PATENT PRODUCT. THE PATENT NUMBER ARE U.S. PAT. 6,840,817 AND U.S. PAT. 5,736,910 AND U.S. PAT. 7,123,117.

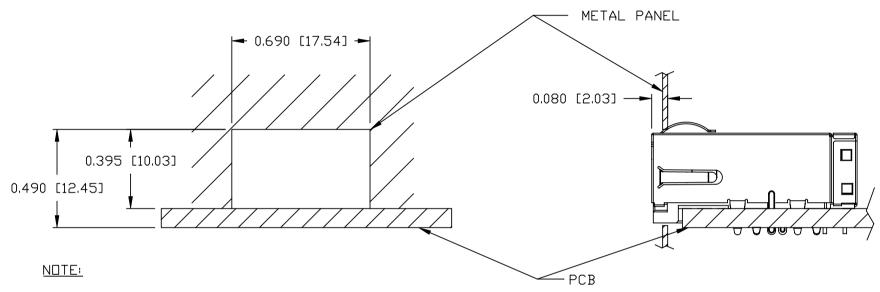
ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM.		[ ] METRIC DIM. AS	REFERENCE
ANTON LIAO	2012-01-18		L835-1X1T-43	TOL. I	N INCH	UNIT : INCH [mm]	REV. : PB
DRAWN BY	DATE	(NanoJack)	FILE NAME	.XX	±0,015	SCALE: N/A	SIZE: A4
WU ZHIHENG	2012-01-18	PATENTED	L835-1X1T-43_PB.DWG	.XXX	±0.010	$\oplus$	PAGE: 3
DC002(2)081111	This document is electronically generated. This is a controlled copy if used internally						





### SUGGESTED PANEL OPENING





THE DISTANCE OF PANEL INSIDE SURFACE RELATIVE TO FRONT SURFACE OF PART IS ONLY A SUGGESTION. IN CASE THIS DISTANCE IS DIFFERENT, THE REQUIRED PANEL OPENING DIMENSIONS CHANGE ACCORDINGLY.

#### PACKING INFORMATION

PACKING TRAY : 0200-9999-M3(TOP)

0200-9999-M4(BOTTOM)

PACKING QUANTITY: 65 PCS FINISHED GOODS PER TRAY.

12 TRAYS (780 PCS FINISHED GOODS) PER CARTON BOX.

NOTE: CARDBOARDS ARE PLACED BETWEEN LAYERS OF PACKING TRAY INSIDE CARTON BOX.

(INCLUDE THE UPPERMOST AND LOWERMOST TRAY)

ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM.		[ ] METRIC DIM. AS REFERENCE		Γ
ANTON LIAO	2012-01-18	gigabit MagJack®	L835-1X1T-43	TOL. I	N INCH	UNIT : INCH [mm]	REV. : PB	l
DRAWN BY	DATE	(NanoJack)	FILE NAME	.XX		SCALE: N/A	SIZE: A4	ı
WU ZHIHENG	2012-01-18	PATENTED	L835-1X1T-43_PB.DWG	.XXX	±0.004		PAGE: 5	

