

1. MATERIALS:
 PLASTIC HOUSING: BLACK, THERMOPLASTIC FLAMMABILITY RATING UL 94V-0
 SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL,
 POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS,
 CONTACTS: PHOSPHOR BRONZE, 1.27um MIN OVERALL NICKEL
 UNDERPLATE WITH SELECT 1.27um MIN GOLD AT MATING INTERFACE AND
 2.54um MIN MATTE TIN ON SOLDER TAILS.
 LED: DIFFUSED EPOXY LENS, CARBON STEEL LEAD FRAME TAILS OF LED
 ARE PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL
 UNDERPLATE OVER 1.02um MIN COPPER UNDERPLATE. POST-PLATED WITH
 2.54um MIN MATTE TIN AND/OR SAC SOLDER DIP OR PURE TIN SOLDER DIP

2. MAGNETICS
 APPLICATION: 10/100/1000 BASE-T, PoE PLUS
 IMPEDANCE: 100 OHMS
 TURNS RATIO (CHIP:CABLE): 1:1 ALL FOUR PAIRS
 OPEN CIRCUIT INDUCTANCE (OCL): ALL CHANNELS,
 350uH MIN@100kHz, 0.1VRMS, WITH 8mADC BIAS FROM 0°C TO 70°C,
 120uH MIN@100kHz, 0.1VRMS, 19mADC BIAS FROM 0°C TO 70°C,
 ACROSS RJ1-RJ2 & RJ3-RJ6
 ALL FOUR PAIRS BI-DIRECTIONAL
 POE CURRENT: 600mADC MAX
 PERFORMANCE @ 25°C:
 INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
 RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
 12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz
 CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
 33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
 COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
 ISOLATION VOLTAGE: 2250VDC(MAX) FOR 60 SECONDS WITH A RISE TIME OF
 500V/SEC AND WITH ALL PORTS CONNECTED.

3. PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE LOCATED
 IN APPROXIMATE AREA SHOWN. DATE CODE: "YY" IS YEAR, "WW" IS
 WORK WEEK, "D" IS DAY OF WEEK, WITH SUNDAY=1

4. TRP LOGO AND AGENCY APPROVAL LOGO ARE
 LOCATED IN APPROXIMATE AREA SHOWN.

5. OPERATING TEMP: FROM 0°C TO +70°C.

6. RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.

7. INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND
 SUPPORT AUTO-MDI/MDIX.

8. DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.

9. BASIC DIMENSION ESTABLISHED BY CUSTOMER, BUT MAY NOT BE
 GREATER THAN 5.08mm.

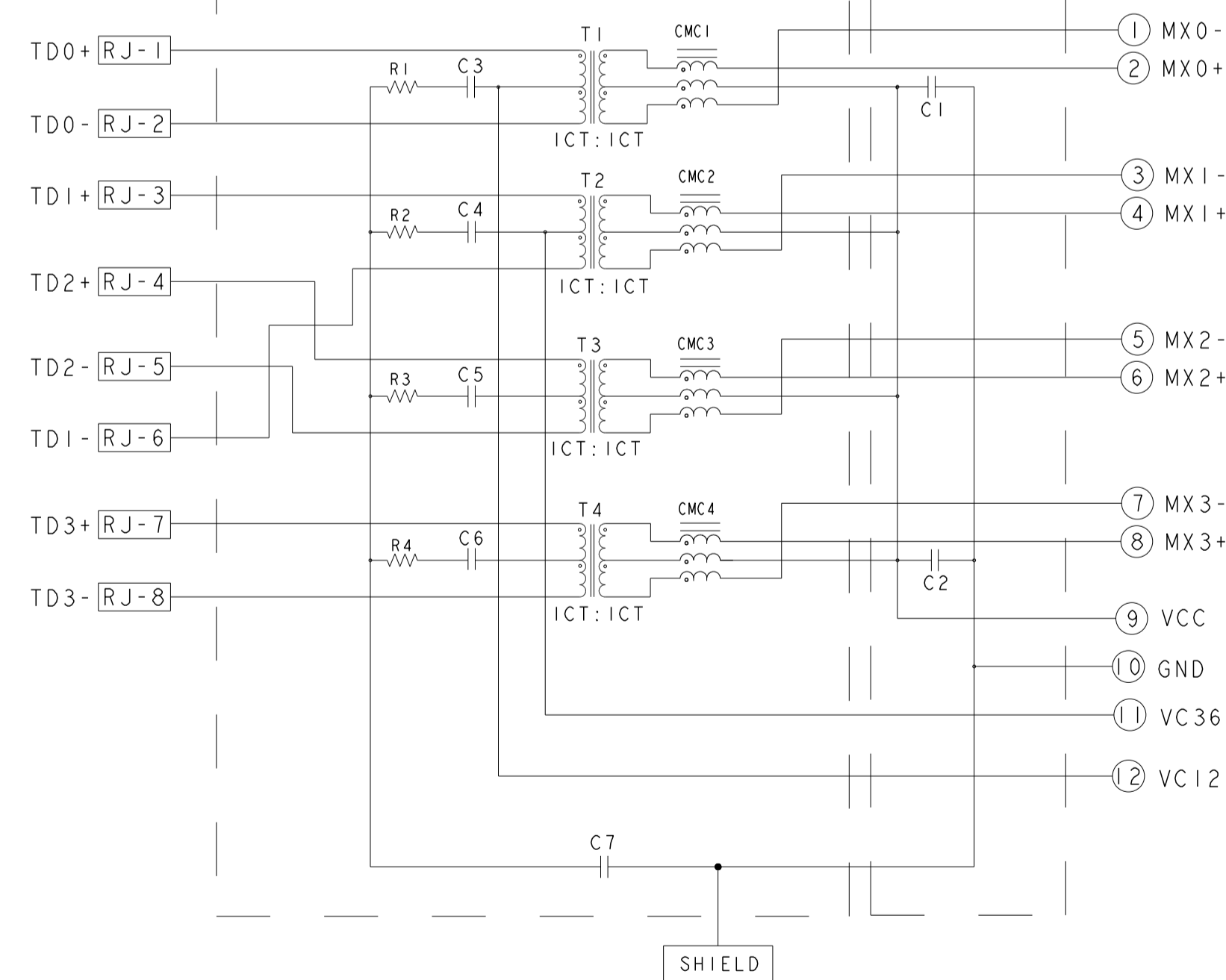
10. LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX 20mA
 LED COLOR: DOMINANT WAVELENGTH (λD): GREEN 568 nm TYP. @ IF=20mA
 FORWARD VOLTAGE (VF): GREEN 2.2V TYP. @ IF=20mA
 DOMINANT WAVELENGTH (λD): ORANGE 605 nm TYP. @ IF=20mA
 FORWARD VOLTAGE (VF): ORANGE 2.1V TYP. @ IF=20mA

11. THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS,
 PEAK TEMPERATURE 260°C MAX, 10 SECONDS MAX.

S9HG62A GIGABIT PoE PLUS CIRCUIT

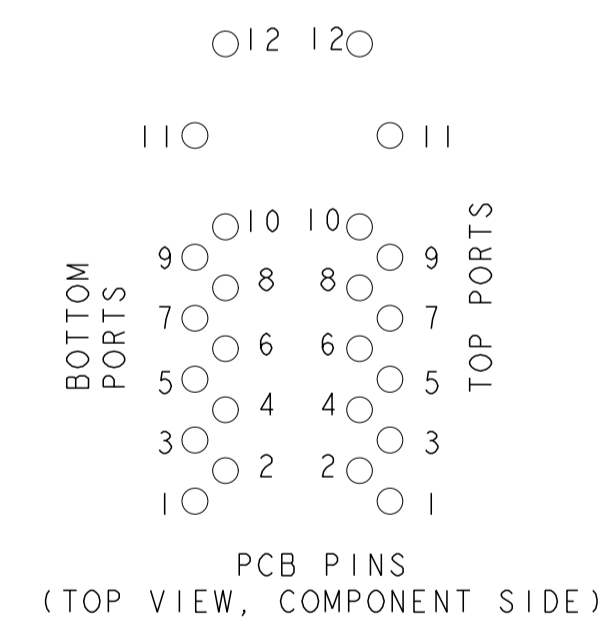
TOP AND BOTTOM PORTS

MAGNETICS BOTTOM PCB

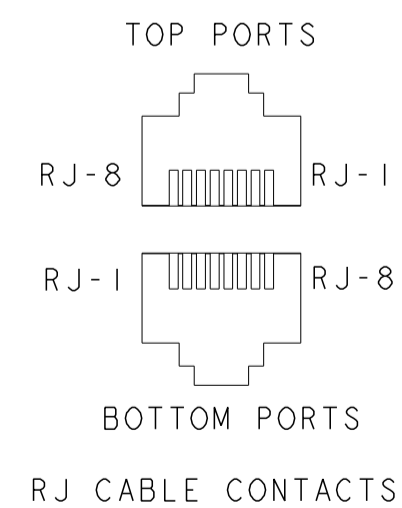


PIN DESIGNATIONS

(REPEAT FOR EACH VERTICAL PAIR OF PORTS)

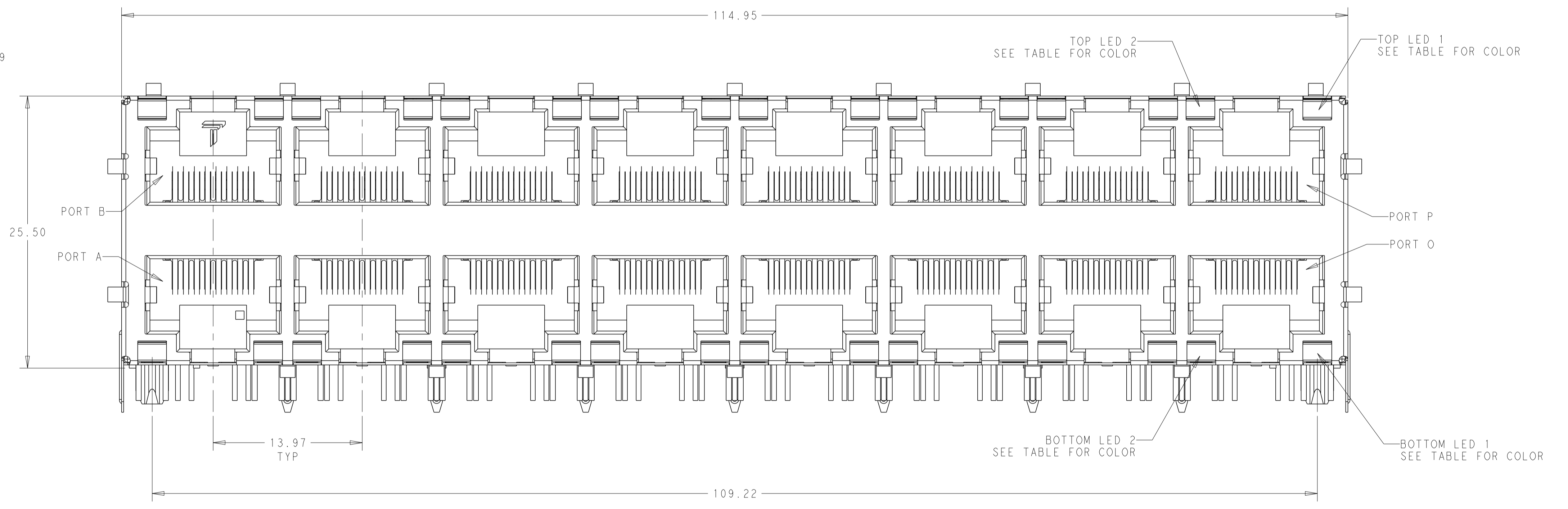
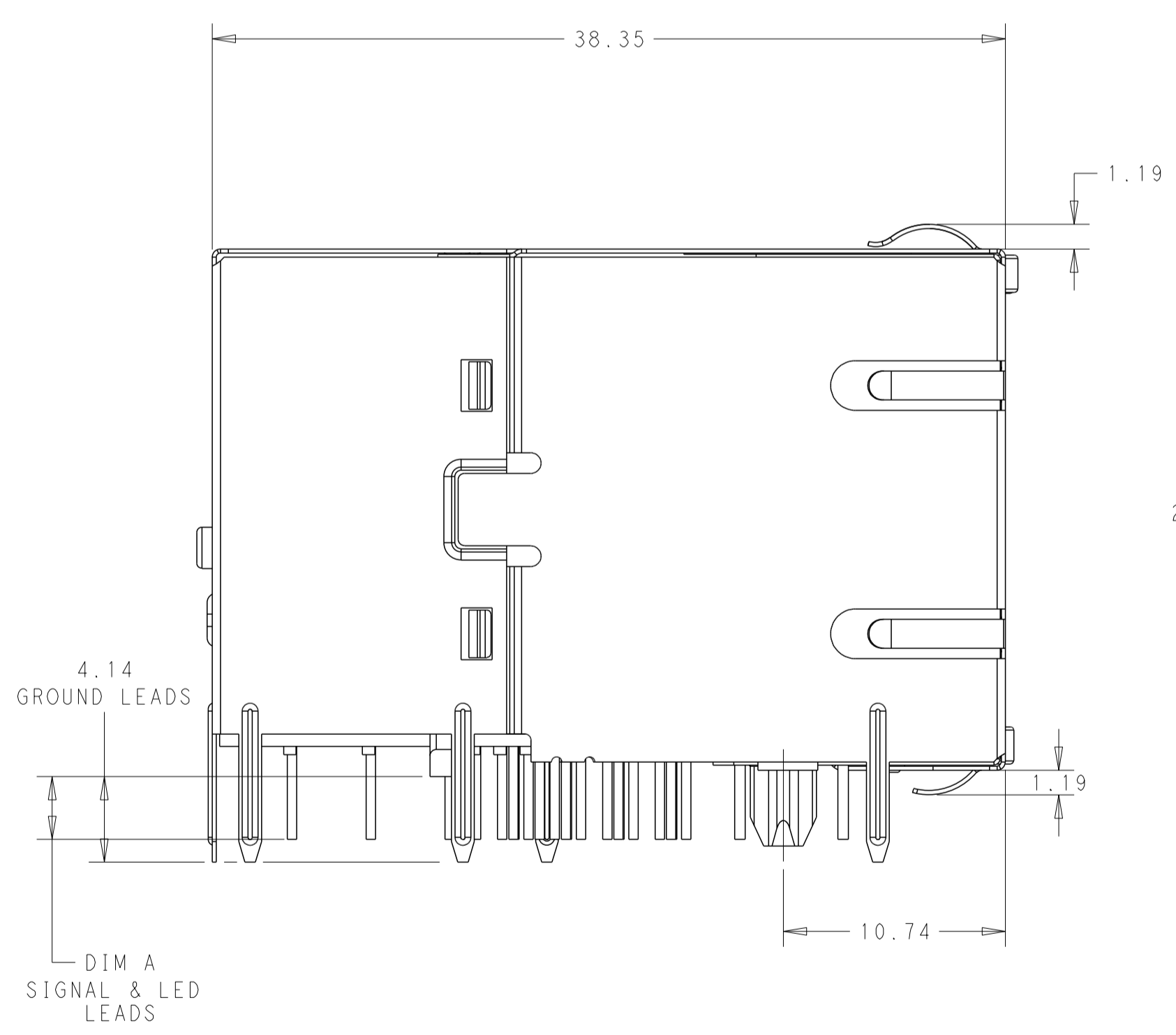
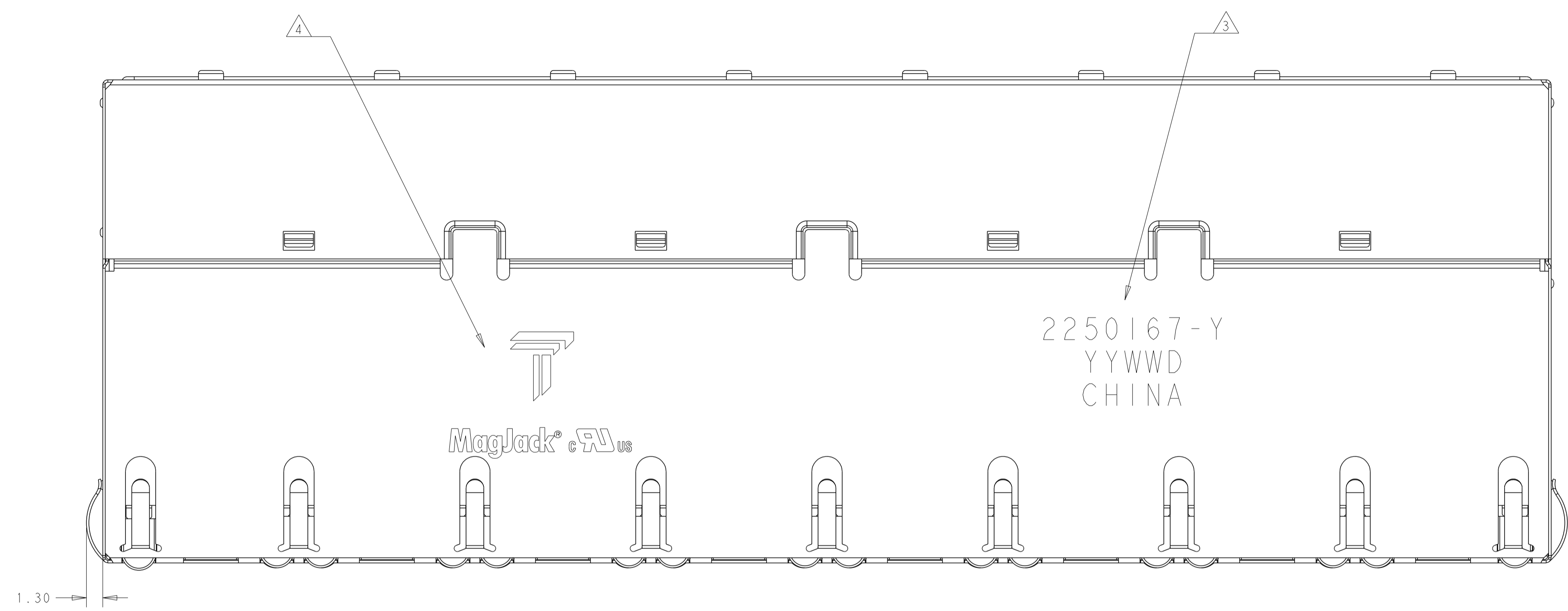


PCB PINS (TOP VIEW, COMPONENT SIDE)

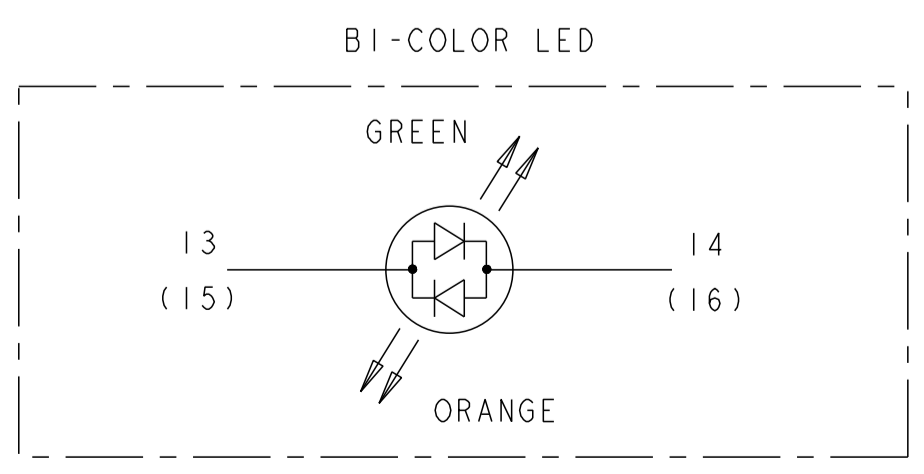
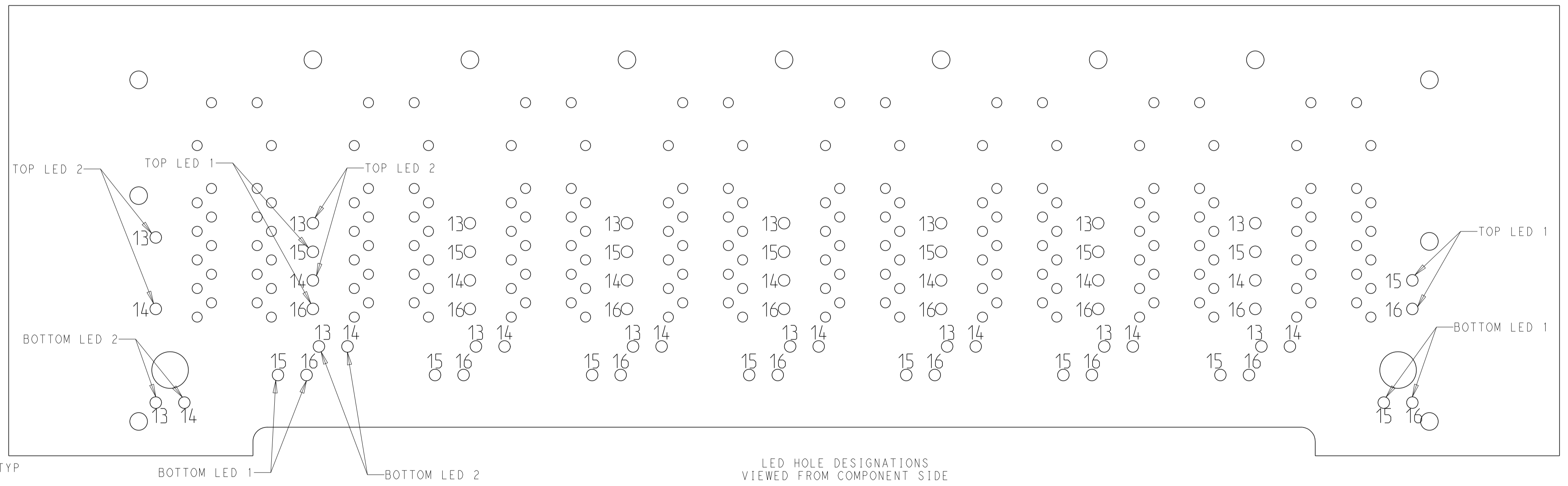
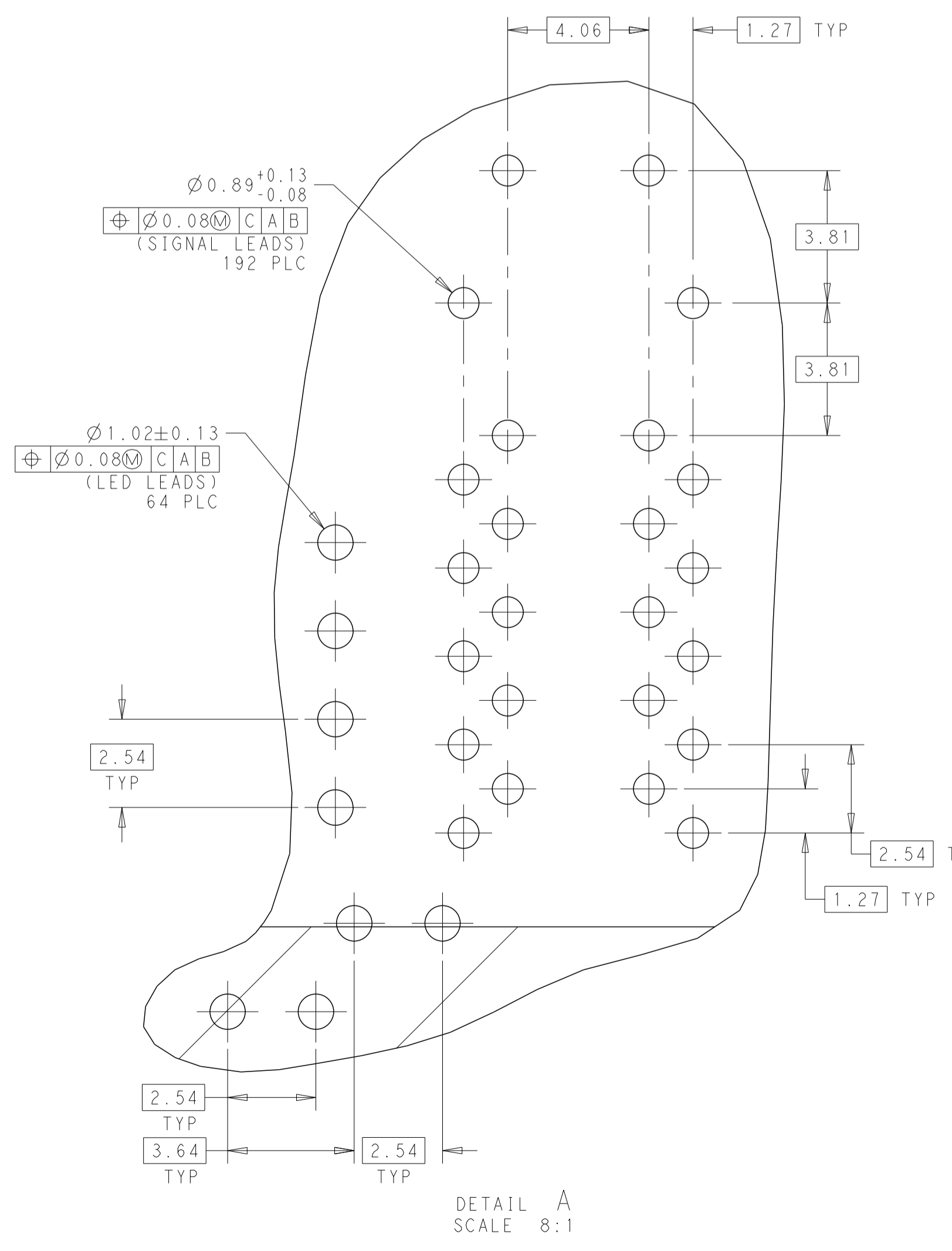
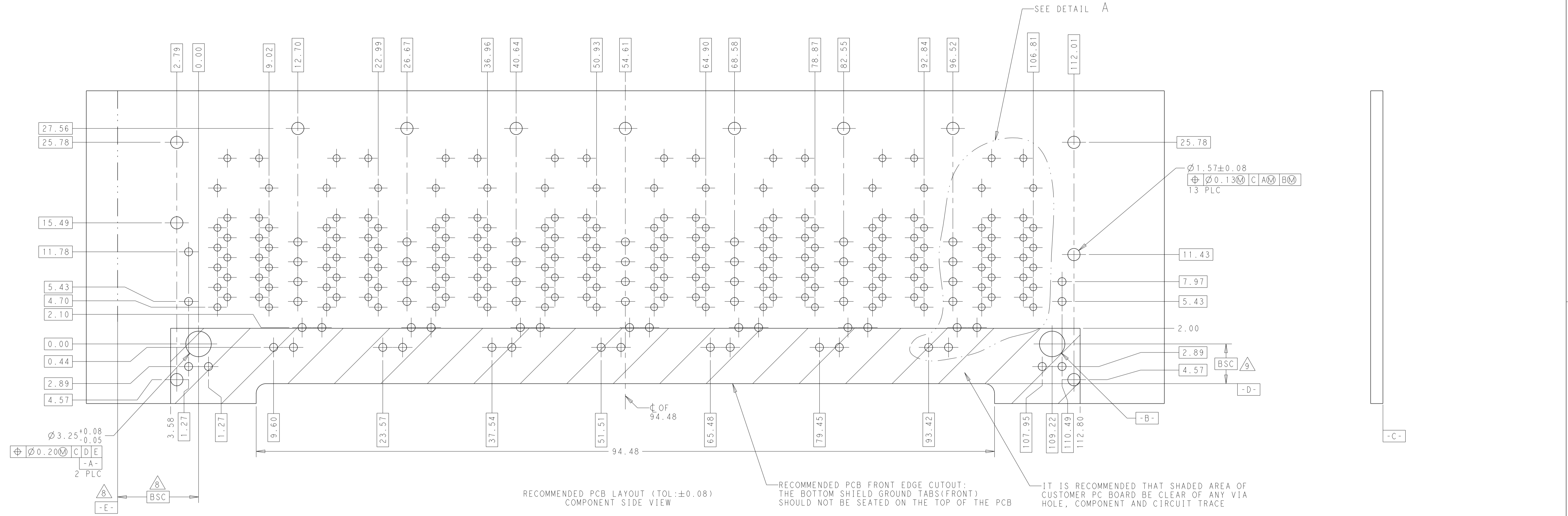


C1 - C2 = 10nF, 50V, X7R, CAPACITORS
 C3 - C6 = 22nF, 100V, CAPACITORS
 C7 = 1000pF, 2kV, CAPACITOR
 R1 - R4 = 75 Ohms, 1/16W, RESISTORS

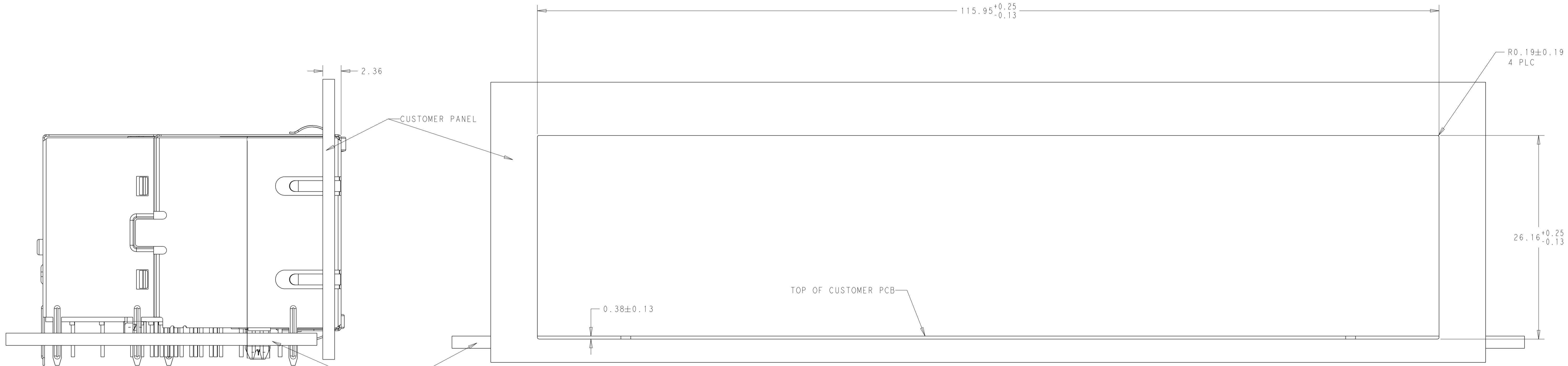
3.56	GRN/ORA	GRN/ORA	GRN/ORA	GRN/ORA	2250167-7
DIM A	BOTTOM LED 2	BOTTOM LED 1	TOP LED 2	TOP LED 1	PART NUMBER
THIS DRAWING IS A CONTROLLED DOCUMENT.					
DIMENSIONS: mm		TOLERANCES UNLESS OTHERWISE SPECIFIED:			
0 PLC ±0.25		1 PLC ±0.25			
2 PLC ±0.25		3 PLC ±0.25			
4 PLC ±		ANGLES ±			
PRODUCT SPEC 108-104004		APPLICATION SPEC			
DWG: CLOUD_ZENG 18MAR2014		CHK: TOMMY_REN 18MAR2014			
APPR: TONY_YUAN 18MAR2014		MODEL NAME			
MAGJACK		DESC: 2X8 S9HG62A GIGABIT PoE+ W/ LED			
STACK PoE+		SIZE: A1		CAGE CODE: 2250167	
CUSTOMER DRAWING		SCALE: 4:1		SHEET 1 OF 4	



THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN: CLOUD_ZENG 18MAR2014	<p>trp CONNECTOR DONGGUAN CHINA</p>
		CHK: TOMMY_REN 18MAR2014	
		APP'D: TONY_YIJIAN 18MAR2014	
DIMENSIONS: mm 		TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ± 4 PLC ± ANGLES ± APPLICATION SPEC ±	MODEL NAME: MAGJACK STACK PoE+
PRODUCT SPEC: 108-104004		DESC: 2X8 S9HG62A GIGABIT PoE+ W/ LED SIZE: A1 CAGE CODE: C=2250167 DRAWING NO: - RESTRICTED TO: - SCALE: 4:1 SHEET 2 OF 4 REV 2	
		CUSTOMER DRAWING	

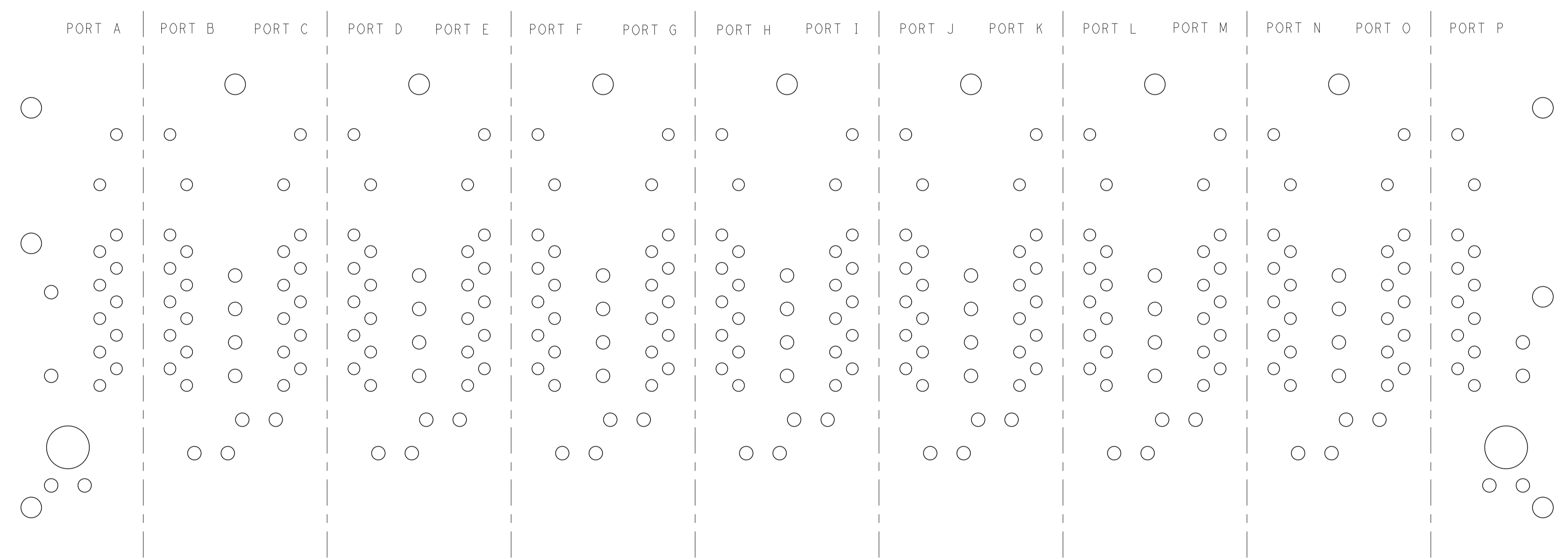


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DIMENSIONS: mm		CHK: TOMMY_REN 18MAR2014		
		APD: TONY_YUAN 18MAR2014		
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ±0.25 4 PLC ±0.25 ANGLES ±°		MODEL NAME: MAGJACK STACK PoE+		DESC: 2X8 S9HG62A GIGABIT PoE+ W/ LED
PRODUCT SPEC: 108-104004		APPLICATION SPEC: -		SIZE: A1
CUSTOMER DRAWING		SCALE: 4:1		SHEET 3 OF 4 REV 2


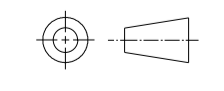


SHOWN WITH PANEL AND CUSTOMER PCB ASSEMBLY

SUGGESTED PANEL CUTOUT DIMENSIONS (TOL: ±0.10)



PORT ASSIGNMENT COMPONENT SIDE VIEW

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DIMENSIONS: mm		CHK: TOMMY_REN 18MAR2014	
		APP'D: TONY_YUAN 18MAR2014	
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ± 4 PLC ± ANGLES ±°		MODEL NAME: MAGJACK STACK PoE+	DESC: 2X8 S9HG62A GIGABIT PoE+ W/ LED
PRODUCT SPEC: 108-104004	APPLICATION SPEC: -	CUSTOMER DRAWING	SIZE: A1 CAGE CODE: - DRAWING NO: C=2250167 RESTRICTED TO: - SCALE: 4:1 SHEET 4 OF 4 REV 2