

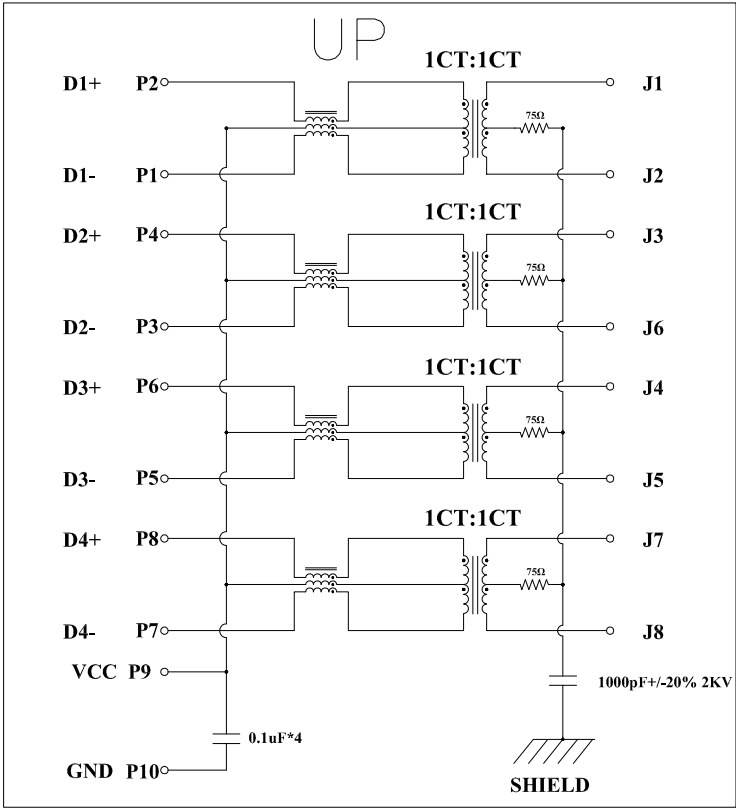
1. Mates With Modular Plug Conforming To Fcc Part 68, Subpart F.



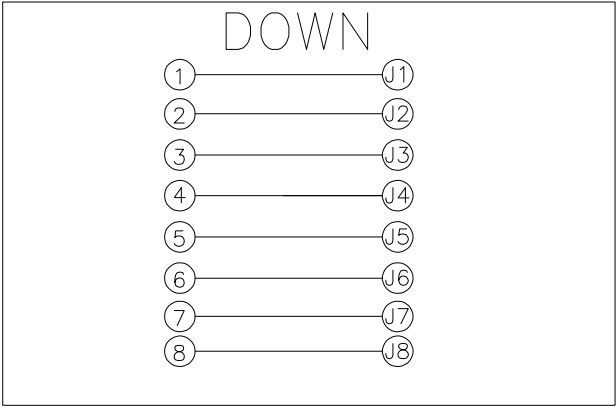
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Electrical Specification @ 25 C °

RoHS



PARAMETER	SPECIFICATIONS	
URNS RATIO	1.00 ± 2%	
POLARITY	PER SCHEMATIC	
INSERTION LOSS (DATA CHANNELS)	100 KHz–125MHz	
	1.2 dB MAX	
RETURN LOSS (Z OUT=100 OHM ±1%)	0.5–40 MHz	40 MHz–100 MHz
	18 dB MIN	12+20LOG(f/80)dB MIN
CROSS TALK LOSS	1–40 MHz	1–100 MHz
	35 dB MIN	33–20LOG(f/50)dB MIN
COMMON MODE REJECTION RATIO	1–100 MHz	
	30 dB MIN	
INDUCTANCE (OCL)	350 uH MIN @ 100 KHz, 100 mV,8mA DC BIAS	
HIPOT–OUTPUT ISOLATION	2250 VDC MIN @ 60 SECONDS	



DRAWN	24Apr2024
JARRY YE / FRANK LIU	
CHECKED	24Apr2024
BILL LIU	
APPROVED	24Apr2024
TONY YUAN	

TITLE
Gigabit MagJack ®
(WITH LED)
PATENTED

DRAWING NO.	P01–9CD0
FILE NAME	P01–9CD0_1.DWG

UNLESS OTHERWISE NOTED, TOL:			
X.	±0.38	SIZE:	A4
.X	±0.38	UNIT:	mm
.XX	±0.25	SCALE:	N/A
.XXX	±0.13	PAGE: 2/3	REV.: 1



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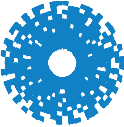
CHANGE HISTORY						
ISSUE	DATE	DESCRIPTIN OF CHANGE	REASON	DRAWN	APPROVAL	ECN#
1	24APR2024	—	INITIAL RELEASE	JARRY YE	TONY YUAN	—

DRAWN	24Apr2024
JARRY YE /	FRANK LIU
CHECKED	24Apr2024
BILL LIU	
APPROVED	24Apr2024
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