xRSB-80T07L Isolated DC-DC Converter

The xRSB-80T07L series are isolated DC/DC converters that operate from a nominal 48 VDC source. These secondary side control units will provide up to 60 W of output power from a nominal 48 VDC input.

Features include start-up into pre-biased load, remote on/off, over current protection and under voltage lockout.

These converters are provided in an industry standard sixteenth brick package.

Key Features & Benefits

- 48 VDC Input
- 7.5 VDC @ 8 A Output
- 1/16th Brick Converter
- Isolated
- Fixed Frequency (400 kHz)
- High Efficiency
- High Power Density
- Low Cost
- Basic Insulation
- Input Under-Voltage Lockout
- Start-up into Pre-biased Load
- OCP/SCP
- Over Temperature Protection
- Remote On/Off
- Output Voltage Trim
- Positive/Negative Remote Sense
- Output Over-Voltage Protection with Auto-recovery
- Secondary Side Control for Fast Transient Response and High Reliability
- Approved to UL/CSA 62368-1
- Approved to IEC/EN 62368-1
- Class II, Category 2, Non-Isolated DC/DC Converter (refer to IPC-9592B)

Applications

- Networking
- Computers and Peripherals
- Telecommunications







1. MODEL SELECTION

MODEL NUMBER	OUTPUT VOLTAGE	INPUT VOLTAGE	MAX. OUTPUT CURRENT	MAX. OUTPUT POWER	TYPICAL EFFICIENCY
0RSB-80T07LG					
SRSB-80T07LG	7.5 VDC	36 - 75 VDC	8 A	60 W	90.5%
SRSB-80T07LR					

PART NUMBER EXPLANATION

x	R	SB	-	80	Т	07	L	x
Mounting Type	RoHS Status	Series Name		Output Power	Input Range	Output Voltage	Active Logic	Package Type
0 - Through Hole Mount		1/16 th					Active Low, with	G – Tray Package
S – Surface Mount	RoHS	Brick		60 W	36 – 75 V	7.5 V	HSK and Fins	R - Tape and Reel Package

2. ABSOLUTE MAXIMUM RATINGS

PARAMETER	DESCRIPTION	MIN	TYP	MAX	UNITS
Input Voltage	Continuous, non-operating	-0.3	-	75	V
Input Transient Voltage	100 ms maximum	-	-	100	V
Remote On/Off		-0.3	-	18	V
I/O Isolation Voltage		1500	-	-	V
Ambient Temperature		-40	-	85	°C
Storage Temperature		-55	-	125	°C
Altitude		-	-	2000	m

NOTE: Ratings used beyond the maximum ratings may cause a reliability degradation of the converter or may permanently damage the device.

3. INPUT SPECIFICATIONS

All specifications are typical at 25°C unless otherwise stated.

PARAMETER	DESCRIPTION	MIN	TYP	MAX	UNIT
Operating Input Voltage		36	48	75	V
Input Current (full load)		-	-	2.5	А
Input Current (no load)		-	30	70	mA
Remote Off Input Current		-	2	5	mA
Input Reflected Ripple Current (rms)	With simulated source impedance of 10 μ H, 5 Hz to 20 MHz. Use a 100 μ F/100 V	-	7	15	mA
Input Reflected Ripple Current (pk-pk)	electrolytic capacitor with ESR=1 ohm max, at 200 kHz @ 25°C	-	30	50	mA
I ² t Inrush Current Transient		-	-	1	A ² s
Turn-on Voltage Threshold		32.5	-	35.5	V
Turn-off Voltage Threshold		31	-	34	V

CAUTION: This converter is not internally fused. An input line fuse must be used in application. Recommend a fast-acting fuse with maximum rating of 5 A on system board. Refer to the fuse manufacture's datasheet for further information.

NOTES: This converter has internal L-C (0.47 μF - 0.33 μH - 2.35 $\mu\text{F})$ filter.



4. OUTPUT SPECIFICATIONS

All specifications are typical at nominal input, full load at 25°C unless otherwise stated.

PARAMETER	DESCRIPTION	MIN	TYP	MAX	UNIT
Output Voltage Set Point	Vin = 48V, Io = 50% load	7.350	7.500	7.650	V
Load Regulation	Vin = 48 V, lo = 0%-100% load	-	±12	±24	mV
Line Regulation	Vin = 36 to 75V	-	±12	±24	mV
Regulation Over Temperature	Ambient temperature = -40°C to 85°C	-	±30	±50	mV
Ripple and Noise (pk-pk)	0-20 MHz BW, with a 1 μ F ceramic and a	-	90	120	mV
Ripple and Noise (rms)	10 µF Tan capacitor at output.	-	20	30	mV
Output Ripple and Noise (pk-pk) under worst case	Over entire operating input voltage range, load and ambient temperature	-	-	150	mV
Output Current Range		0	-	8	А
Output DC Current Limit		8.5	10	11.5	А
Short Circuit Surge Transient		-	-	3	A ² s
Rise Time (from ON/OFF or Vin)	From 10% to 90% of Vo	-	-	80	ms
Turn on Delay Time	Enable from Vin Turn on time from Vin to 90% of Vo Enable from ON/OFF	-	-	100	ms
	Turn on time from ON/OFF to 90% of Vo	-	-	100	ms
Overshoot at Turn on		-	0	3	%
Output Capacitance	Typically 50% ceramic + 50% electrolytic capacitors	0	-	3300	μF
Transient Response					
△V 50%~75% of Max Load		-	-	250	mV
Settling Time	di/dt = 0.1 A/ μ s, Vin = 48 VDC, Ta = 25°C	-	-	300	μs
△V 75%~50% of Max Load	Load capacitor = $1000 \ \mu F = 50\%$ ceramic + 50% electrolytic capacitors	-	-	250	mV
Settling Time		-	-	300	μs



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5. GENERAL SPECIFICATIONS

DESCRIPTION	MIN	TYP	MAX	UNIT
The efficiency is measured at Vin = 48 V, full load and Ta = 25° C	89	90.5	-	%
	-	400	-	kHz
Calculated Per Bell Core SR-332 (Vin = 48 V, Vo = 7.5 V, Io = 8 A, Ta = 25°C, FIT = 10^9 /MTBF)	-	TBC	-	-
	-	125	-	°C
This voltage is achieved by trimming up output slowly	-	10	-	V
	-	15.5	-	g
	1.3	30 x 0.90 x 0	.453	inch
	33.0	mm		
	1.3	inch		
	33.0	mm		
	-	-	1500	V
	10M	-	-	Ohm
	-	1000	-	pF
	The efficiency is measured at Vin = 48 V, full load and Ta = 25° C Calculated Per Bell Core SR-332 (Vin = 48 V, Vo = 7.5 V, Io = 8 A, Ta = 25° C, FIT = 10^{9} /MTBF) This voltage is achieved by trimming up	The efficiency is measured at Vin = 48 V, 89 full load and Ta = 25°C - Calculated Per Bell Core SR-332 - (Vin = 48 V, Vo = 7.5 V, Io = 8 A, Ta = 25°C, - FIT = 10 ⁹ /MTBF) - This voltage is achieved by trimming up output slowly - For 0RSB-80T07L 33.0 For SRSB-80T07L 33.0 - -	$\begin{array}{c c c c c c c c } The efficiency is measured at Vin = 48 V, \\ full load and Ta = 25^{\circ}C & - & 400 \\ \hline & & 400 \\ \hline & & 400 \\ \hline & & & 100 \\ \hline & & & 125 \\ This voltage is achieved by trimming up \\ output slowly & - & 10 \\ \hline & & 15.5 \\ For 0RSB-80T07L & & 1.30 \times 0.90 \times 0 \\ \hline & & 33.02 \times 22.86 \times 1 \\ \hline & & & 1.30 \times 0.90 \times 0 \\ \hline & & & 33.02 \times 22.86 \times 1 \\ \hline & & & & 1.30 \times 0.90 \times 0 \\ \hline & & & & & & 1.30 \times 0.90 \times 0 \\ \hline & & & & & & & & \\ For SRSB-80T07L & & & & & & \\ \hline & & & & & & & & & \\ \hline & & & &$	$\begin{array}{c c c c c } The efficiency is measured at Vin = 48 V, \\ full load and Ta = 25^{\circ}C & & & & & & & & & & & & & & & & & & &$

6. EFFICIENCY DATA

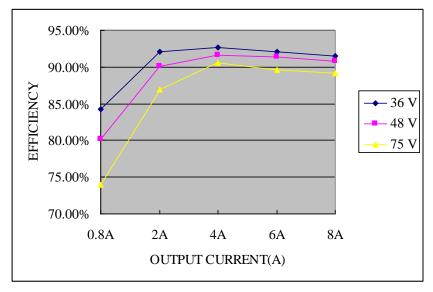


Figure 1. Efficiency data



7. REMOTE ON/OFF

PARAMETER		DESCRIPTION	MIN	TYP	MAX	UNIT
Signal Low (Unit Off)	Active Low	Remote On/Off nin is onen the module is off	-0.3	-	0.8	V
Signal High (Unit On)	Active Low	Remote On/Off pin is open, the module is off	2.4	-	18	V
Current Sink			0	-	1	mA

Recommended remote on/off circuit for active low

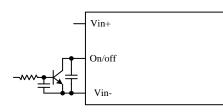


Figure 2. Control with open collector/drain circuit

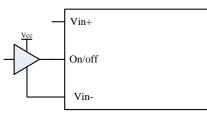


Figure 4. Control with logic circuit

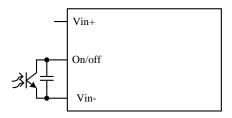


Figure 3. Control with photocoupler circuit

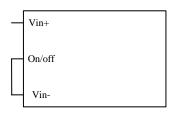


Figure 5. Permanently on





8. REMOTE SENSE

This module has remote sense compensation feature. It can minimize the effects of resistance between output and load in system layout and facilitate accurate voltage regulation at load terminals or other selected point.

- 1. The remote sense lines carry very little current and hence do not require a large cross-sectional area.
- 2. This module compensates for a maximum drop of 5% of the nominal output voltage.

3. If the unit is already trimmed up, the available remote sense compensation range should be correspondingly reduced. The total voltage increased by trim and remote sense should not exceed 5% of the nominal output voltage.

4. When using remote sense compensation, all the resistance, parasitic inductance and capacitance of the system are incorporated within the feedback loop of this module which can make an effect on the module's compensation, affecting the stability and dynamic response. A 0.1 uF ceramic capacitor can be connected at the point of load to de-couple noise on the sense wires.

5. Recommend the connection of remote sense compensation as below figure. There are a resistor RS+ (100 ohm) from Vo+ to Sense+ and a resistor RS- (11.3 ohm) from Vo- to Sense- inside of this module.

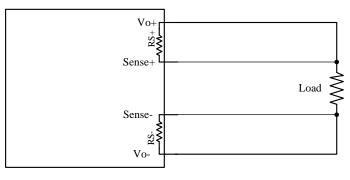


Figure 6.

6. If not using remote sense compensation, please connect sense directly to output at module's pin, that is, connect sense+ to Vo+ and sense- to Vo- at module's pin, the shorter the better. see below figure.

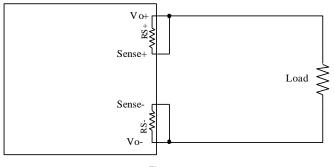


Figure 7.



9. OUTPUT TRIM EQUATIONS

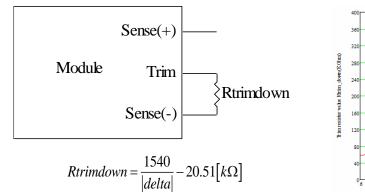
Equations for calculating the trim resistor are shown below. The trim down resistor should be connected between the Trim pin and Sense (-) pin. The trim up resistor should be connected between the Trim pin and the Sense (+). Only one of the resistors should be used for any given application.

Minimum trim down voltage is 6 V.

Maximum trim up voltage is 9 V.

The total voltage increased by trim and remote sense should not exceed 20% of the nominal output voltage.

Trim Down Test Circuit:



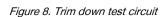
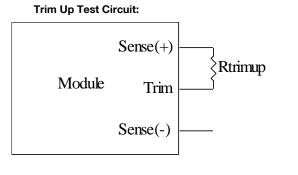
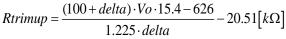
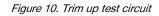


Figure 9. Trim down curve

Adjusted output voltage Vo,adj (V)







NOTE:

$$delta = \frac{(Vo_req-Vo)}{Vo} \times 100 [\%]$$

Vo_req = Desired (trimmed) output voltage [V] Output voltage Vo = 7.5 V



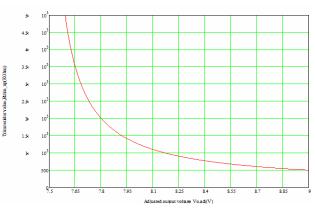


Figure 11. Trim up curve

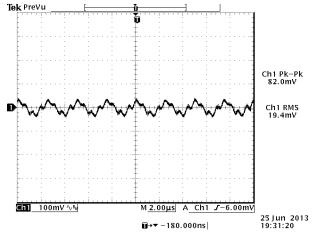
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10. RIPPLE AND NOISE WAVEFORM

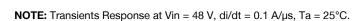




NOTE: Ripple and noise at full load, 48 VDC input, 7.5 VDC / 8 A output and Ta = 25 °C, with a 1 µF ceramic cap at output.

Tek Stop Tek Stop فالمعطانية Ch1 Max 40.0mV Ch1 Max 124mV D Ch1 Min -86.0mV Ch1 Min -216mV Ch2 Rise Ch2 Rise 17.70µs No valid edge 2 2 Ch1 100mV ∿% Ch2 200mV Ω% M 200µs A Ch2 Ն 452mV Ch1 100mV \vert_kCh2 200mV ΩthM 100μs A Ch2 J 456mV 29 Jan 2015 08:38:20 29 Jan 20 08:37:23 29.60 % 29.60 % Figure 13. Vo = 7.5 V, 50% to 75% Load Transients Figure 14. Vo = 7.5 V, 75% to 50% Load Transients

11. TRANSIENT RESPONSE WAVEFORMS





12. STARTUP & SHUTDOWN

RISE TIME

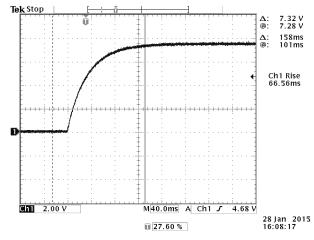
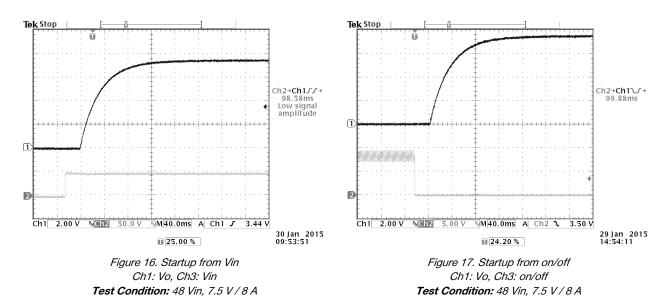


Figure 15. Vin = 48 V, Vo = 7.5 V, Io = 8 A

STARTUP TIME



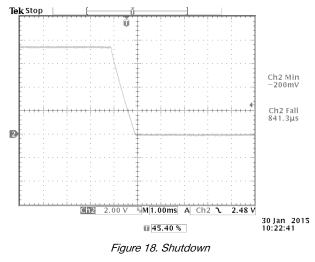


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SHUTDOWN



Test Condition: 48 Vin, 7.5 V / 8 A

13. OVER VOLTAGE PROTECTION

The output over voltage protection consists of circuitry that monitors the voltage on the output terminals. If the voltage on the output terminals exceeds the over voltage protection threshold, the module will shut down into hiccup mode and restart once every 400 ms. The module operates normally when the fault is cleared.

Test setup:

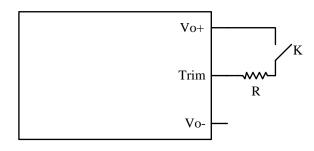


Figure 19.



14. OVER CURRENT PROTECTION

To provide protection in a fault output overload condition, the module is equipped with internal current-limiting circuitry which can endure current limiting for a few milliseconds. If the over current condition persists beyond a few milliseconds, the module will shut down into hiccup mode and restart once every 800 ms. The module operates normally when the output current goes into specified range. The typical average output current is 0.25 A during hiccup.

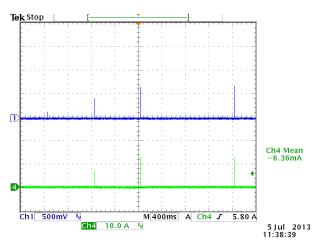
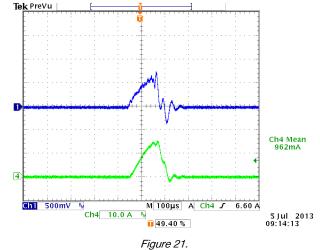


Figure 20. CH1: Output Voltage CH2: Output current waveform **Test condition:** 48Vin, short module output by E-load



Expansion of on time portion of above figure

15. THERMAL DERATING CURVE

Maximum junction temperature of semiconductors derated to 120 °C.

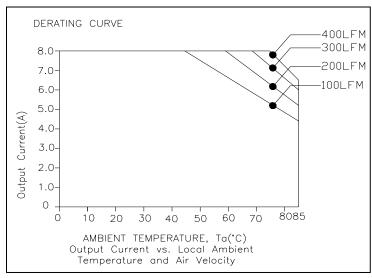


Figure 22. Vo = 7.5 V

The airflow is in either the transverse or longitudinal direction



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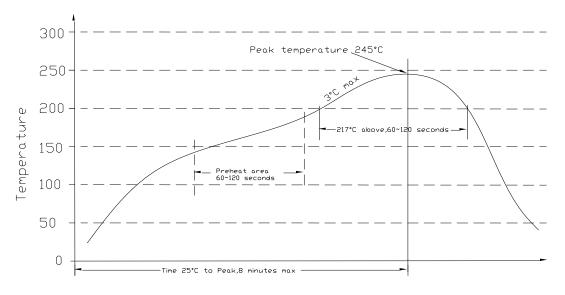
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16. SOLDERING INFORMATION

The xRSB-80T07L modules are designed to be compatible with reflow soldering process. The suggested Pb-free solder paste is Sn/Ag/Cu(SAC). The recommended reflow profile using Sn/Ag/Cu solder is shown in the following. Recommended reflow peak temperature is 245°C while the part can withstand peak temperature of 260°C maximum for 10seconds. This profile should be used only as a guideline. Many other factors influence the success of SMT reflow soldering. Since your production environment may differ, please thoroughly review these guidelines with your process engineers.



Reflow Time (Seconds)

Figure 23. Soldering information

17. MSL RATING

The xRSB-80T07L modules have a MSL rating of 3.

18. STORAGE AND HANDING

The xRSB-80T07L modules are designed to be compatible with J-STD-033 Rev:A (Handling, Packing, Shipping and Use of Moisture /Reflow Sensitive surface Mount devices). Moisture barrier bags (MBB) with desiccant are applied. The recommended storage environment and handling procedure is detailed in J-STD-033.

19. PRE-BAKING

This component has been designed, handled, and packaged ready for Pb-free reflow soldering. If the assembly shop follows J-STD-033 guidelines, no pre-bake of this component is required before being reflowed to a PCB. Our packaging tray can only withstand temperature of 70°C max.



20. MECHANICAL DIMENSIONS

ORSB-80T07L OUTLINE

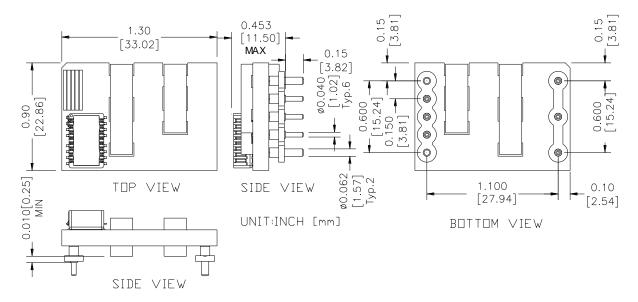


Figure 24. ORSB-80T07L Outline

NOTE:

1) All Pins: Material - Copper Alloy;

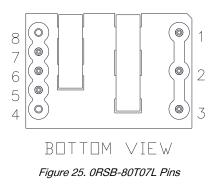
Finish – 3 micro inches minimum Gold over 50 micro inches minimum Nickel plate. 2) Un-dimensioned components are shown for visual reference only.

3) All dimensioned components are shown for visual reference only.
 3) All dimensions in inch [mm]; Tolerances: x.xx +/-0.02 inch [0.51 mm];

x.xxx +/-0.010 inch [0.25 mm].



0RSB-80T07L PIN DEFINITIONS



PIN	FUNCTION	PIN	FUNCTION
1	Vin (+)	5	SENSE (-)
2	Remote On/Off	6	TRIM
3	Vin (-)	7	SENSE (+)
4	Vout (-)	8	Vout (+)

RECOMMENDED PCB PAD LAYOUT

0RSB-80T07L RECOMMENDED PAD LAYOUT

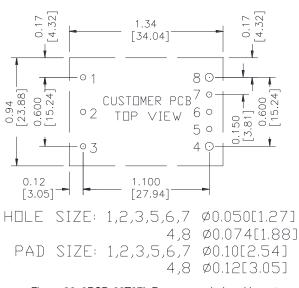
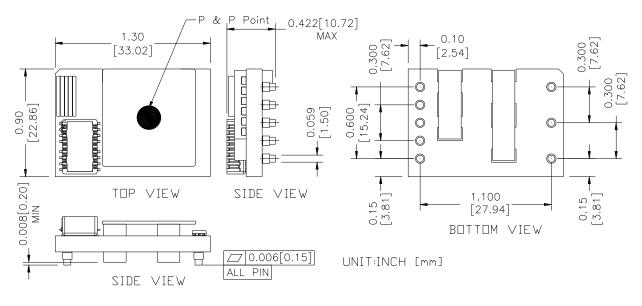
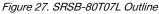


Figure 26. 0RSB-80T07L Recommended pad layout



SRSB-80T07L OUTLINE





NOTE:

1) All Pins: Material - Copper Alloy;

Finish – 3 micro inches minimum Gold over 50 micro inches minimum Nickel plate.

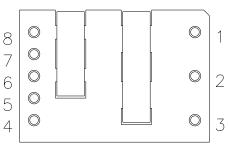
2) Un-dimensioned components are shown for visual reference only.

3) All dimensions in inch [mm]; Tolerances: x.xx +/-0.02 inch [0.51 mm];

x.xxx +/-0.010 inch [0.25 mm].



SRSB-80T07L PIN DEFINITIONS



BOTTOM VIEW

Figure 28. SRSB-80T07L Pins

PIN	FUNCTION	PIN	FUNCTION
1	Vin (+)	5	SENSE (-)
2	Remote On/Off	6	TRIM
3	Vin (-)	7	SENSE (+)
4	Vout (-)	8	Vout(+)

SRSB-80T07L RECOMMENDED PAD LAYOUT

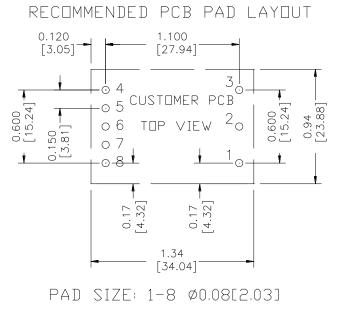
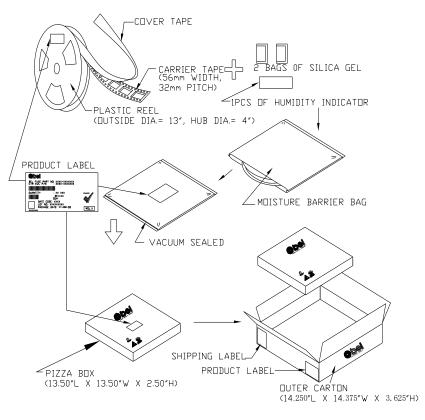


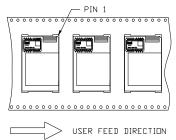
Figure 29. SRSB-80T07L Recommended pad layout



21. PACKAGING INFORMATION

SRSB-80T07LR





DRIENTATION OF COMPONENT INSIDE POCKET

TAPE WIDTH	56mm
POCKET PITCH	32mm
QUANTITY OF COMPONENTS PER REEL	160
PLASTIC REEL DUTER DIAMETER	13 INCHES
PLASTIC REEL HUB DIAMETER	4 INCHES
COMPLY WITH EIA 481-2-A	

Figure 30. SRSB-80T07LR packaging information



ORSB-80T07LG / SRSB-80T07LG

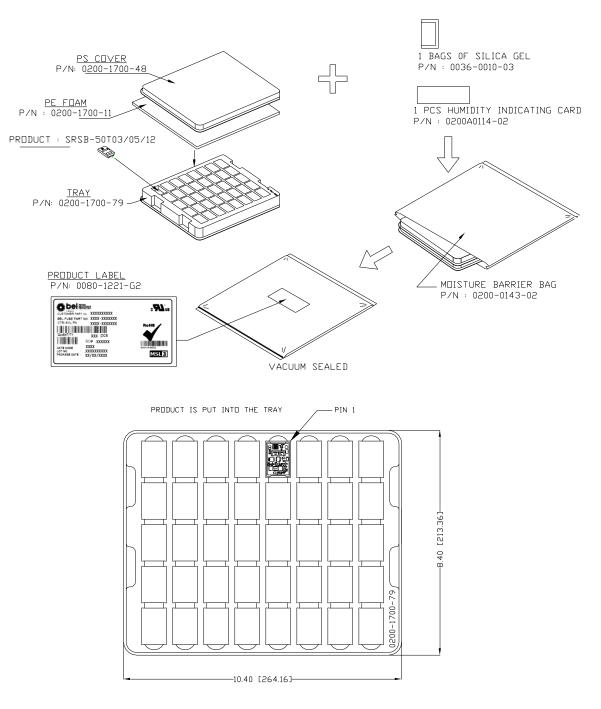


Figure 31. ORSB-80T07LG / SRSB-80T07LG packaging information



22. REVISION HISTORY

DATE	REVISION	CHANGES DETAIL	APPROVAL
2013-07-10	А	First release	J.Yan
2015-02-09	В	Update Output Specs, TR, Startup & Shutdown, Trim, MD	J.Yan
2016-07-27	С	Update	J.Yan
2017-12-21	D	Add reflow profile and tray package	J.Yan
2018-06-25	AE	Update MD	J.Yao
2021-05-28	AF	Add object ID. Update 0RSB-80T07L outline and SRSB-80T07L mechanical dimensions.	J.Yao

For more information on these products consult: tech.support@psbel.com

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TECHNICAL REVISIONS - The appearance of products, including safety agency certifications pictured on labels, may change depending on the date manufactured. Specifications are subject to change without notice.

