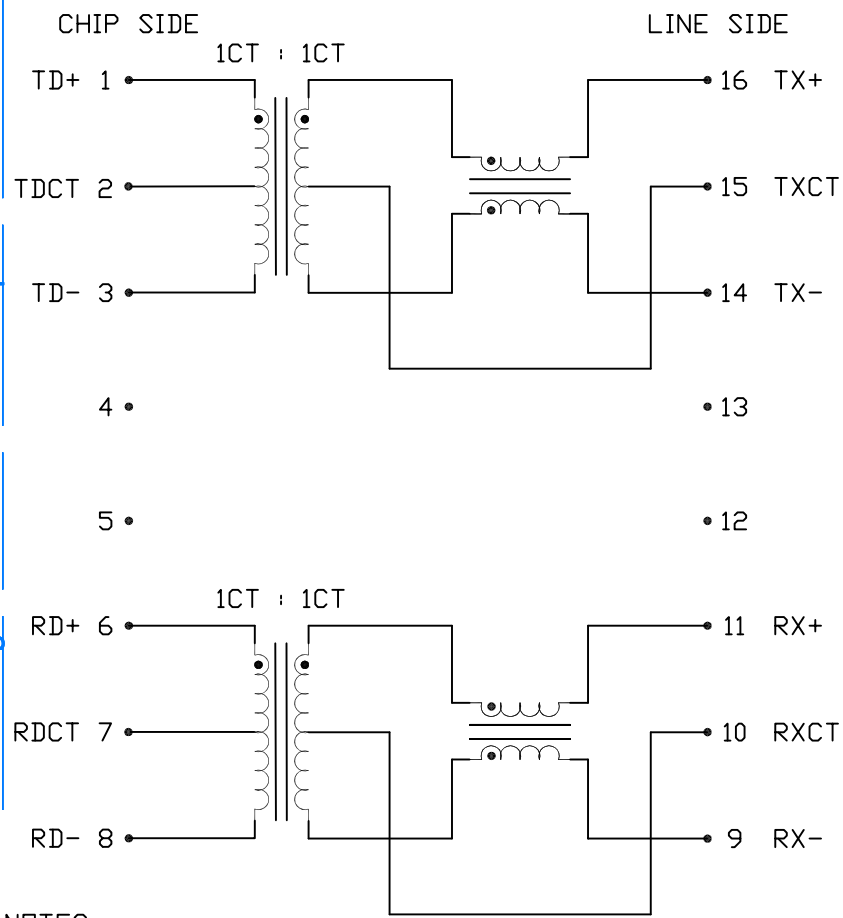


Additional Resources: Product Page 3D Model PCB Footprint Technical Documentation

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SCHEMATIC



NOTES :
THIS PRODUCT HAS BEEN SUPPLYING TO JAPAN CUSTOMERS.
PCN PROCESS NEEDS TO COMPLY WITH 4M CRITERIA.


ELECTRICAL CHARACTERISTICS @25°C



Turns Ratio	1-2-3 : 16-15-14 6-7-8 : 11-10-9	1CT : 1CT 1CT : 1CT
Polarity		PER DOT CONVENTION
DCL	Tx PIN 1-3 Rx PIN 6-8	350µH MIN @100kHz, 100mVrms w/8mA DC BIAS
Leakage Inductance, Le	CHIP SIDE WITH LINE SIDE SHORT	0.3µH MAX @1MHz, 20mVrms
Interwinding Capacitance, Cw/w	CHIP SIDE TO LINE SIDE	30pF MAX @1MHz, 20mVrms
DC Resistance	PIN <1-3> = <6-8>	1.1Ω MAX
Insertion Loss	300kHz - 80MHz	1.0dB MAX
Return Loss	300kHz - 30MHz 30MHz - 60MHz 60MHz - 80MHz	18dB MIN 18-20LOG(F/30MHz) dB MIN 12dB MIN
Common to Common Mode REJ	300kHz - 100MHz	40dB TYP
Common to Differential Mode REJ	300kHz - 100MHz	40dB MIN
Crosstalk	300kHz - 100MHz	40dB MIN
HIPOT		LEAKAGE CURRENT LESS THAN 1mA AT 1500VAC
Operating Temperature	-40°C TO +85°C	

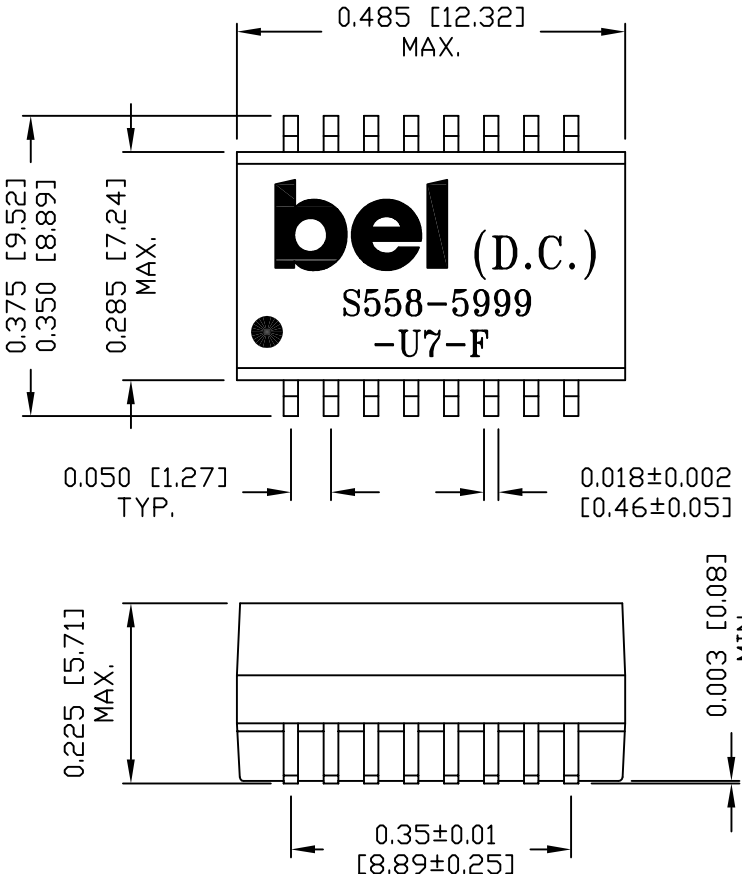
ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM.	[] METRIC DIM. AS REF.
Alice Pang	2021-01-28	10/100BT MAGNETICS MODDLE	S558-5999-U7-F	TOL. IN INCH	UNIT : INCH [mm]
DRAWN BY	DATE		FILE NAME	.X	SCALE : N/A
ZC Guo	2021-01-28	S558-5999-U7-F	S5585999U7FA.DWG	.XX	SIZE : A4
				.XXX	

REV. : A PAGE : 2

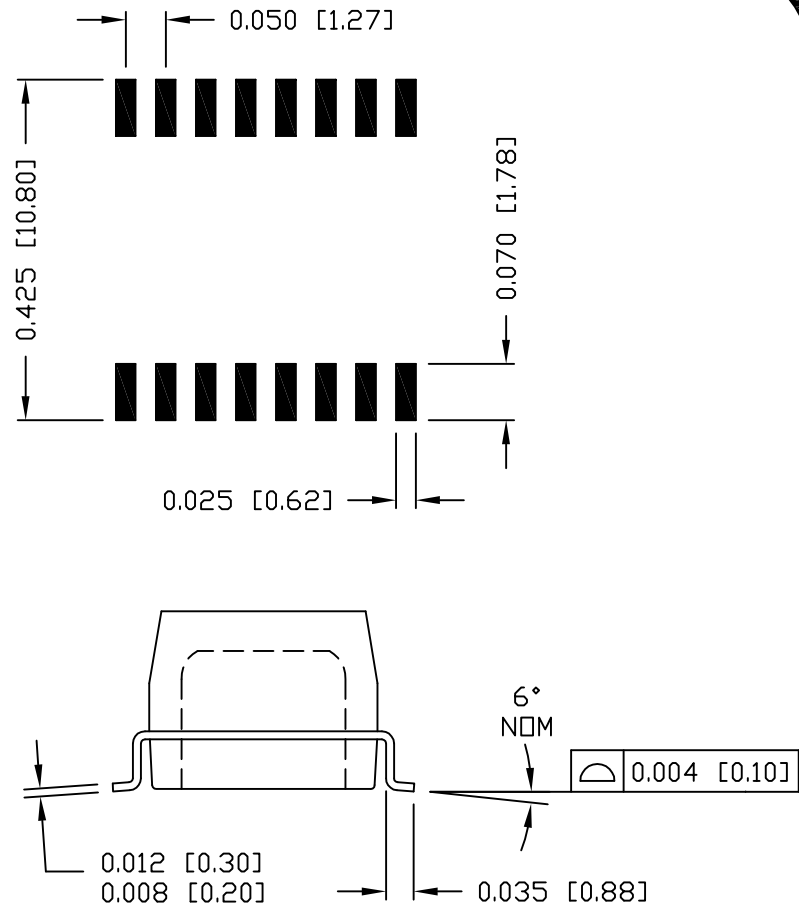

POWER | PROTECT | CONNECT

Additional Resources: Product Page 3D Model PCB Footprint Technical Documentation

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SUGGESTED PCB PAD LAYOUT



- NOTES :
- 1. STANDARD MARKING REFER TO DDC. HAND-WORK-04.
 - 2. PACKAGE CODE: "QBS001".
 - 3. THIS PRODUCT HAS BEEN SUPPLYING TO JAPAN CUSTOMERS. PCN PROCESS NEEDS TO COMPLY WITH 4M CRITERIA.


ORIGINATED BY	DATE
Lawrence Tsang	2021-01-28
DRAWN BY	DATE
ZC Guo	2021-01-28

TITLE
MECHANICAL DRAWING
S558-5999-U7-F

PART NO. / DRAWING NO.
S558-5999-U7-F
FILE NAME
S5585999U7FA.DWG

STANDARD DIM.	[] METRIC DIM. AS REF.
TOL. IN INCH	UNIT : INCH [mm]
.X	SCALE : N/A
.XX	±0.01
.XXX	±0.005

REV. : A PAGE : 3


POWER | PROTECT | CONNECT