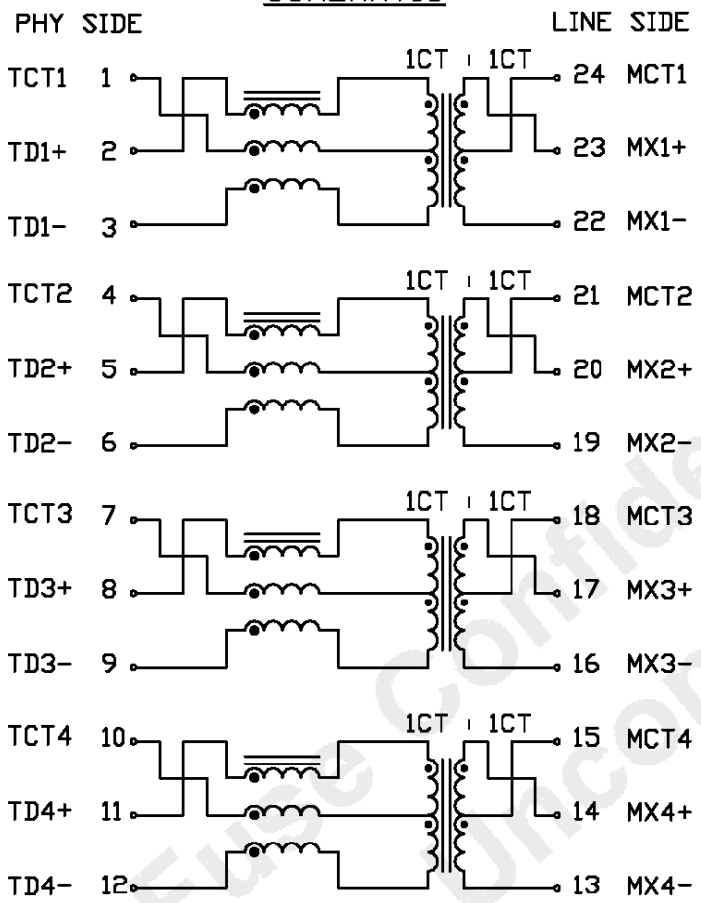
RoHS

SCHEMATIC



ELECTRICAL CHARACTERISTICS @25°C

TURNS RATIO	
(2-1-3) : (23-24-22)	1CT : 1CT ±2%
(5-4-6) : (20-21-19)	1CT : 1CT ±2%
(8-7-9) : (17-18-16)	1CT : 1CT ±2%
(11-10-12) : (14-15-13)	1CT : 1CT ±2%
POLARITY	PER DOT CONVENTION
OCL	100kHz, 100mV
-40°C TO 85°C	180uH MIN WITH 8mA DC BIAS
-40°C TO 85°C	120uH MIN WITH 23mA DC BIAS
INTERWINDING CAPACITANCE, Cww	@1MHz, 20mV 30pF MAX

PARAMETER		MAGNITUDE (IN dB)	FREQUENCY (IN MHZ) START	FREQUENCY (IN MHZ) END
INSERTION LOSS	SDD12 , SDD21	-0.004(FMHz)-0.4 MAX	1	150
RETURN LOSS	SDD11 , SDD22	-20 MIN	1	40
		-20+15LOG(f/40MHz) MIN	40	150
ALIEN NEAR END CROSSTALK	SDDXY'	-46+7LOG(f/125MHz) MIN	1	150
NEAR END CROSSTALK	SDDXY	-35 MIN	1	40
		-35+15LOG(f/40MHz) MIN	40	150
MODE CONVERSION	SDC12	-35 MIN	1	40
		-35+17LOG(f/40MHz) MIN	40	150
	SDC21	-30 MIN	1	40
		-30+11LOG(f/40MHz) MIN	40	150
COMMON MODE REJECTION	SCC21	-25 MIN	1	150

BALANCED DC LINE CURRENT — 1A MAX @57VDC CONTINUOUS

HIPOT — LEAKAGE CURRENT LESS THAN 1mA AT 1500VAC

DESIGNED FOR USE IN 1G/2.5G 802.3BT APPLICATIONS.

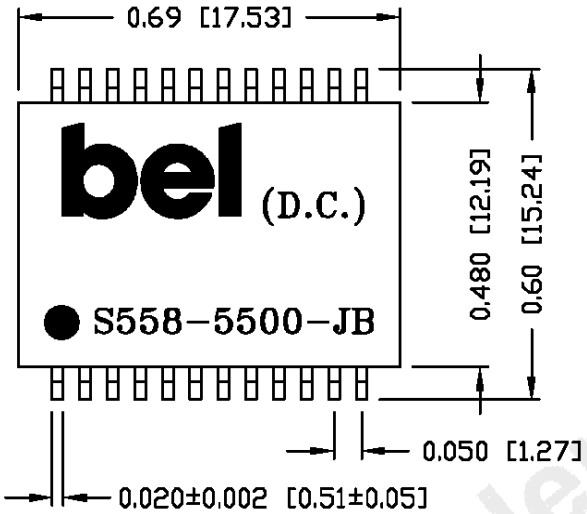
REV. : B | PAGE : 2

ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM. TOL. IN INCH	[ ] METRIC DIM. AS REF.
Alice Pang	2020-06-30	ELECTRICAL SPECIFICATION	S558-5500-JB	.X	UNIT : INCH [mm]
DRAWN BY	DATE		FILE NAME	.XX	SCALE : N/A
ZC Guo	2020-06-30		S5585500JBB.DWGC	.XXX	SIZE : A4

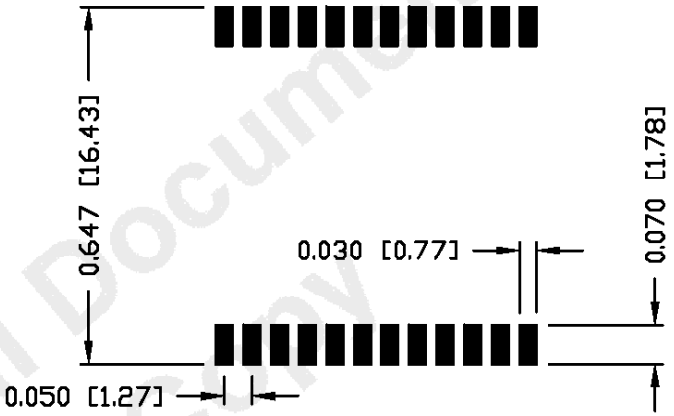
bel POWER | PROTECT | CONNECT

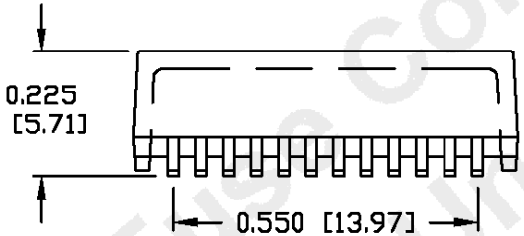
Additional Resources: Product Page 3D Model Technical Documentation

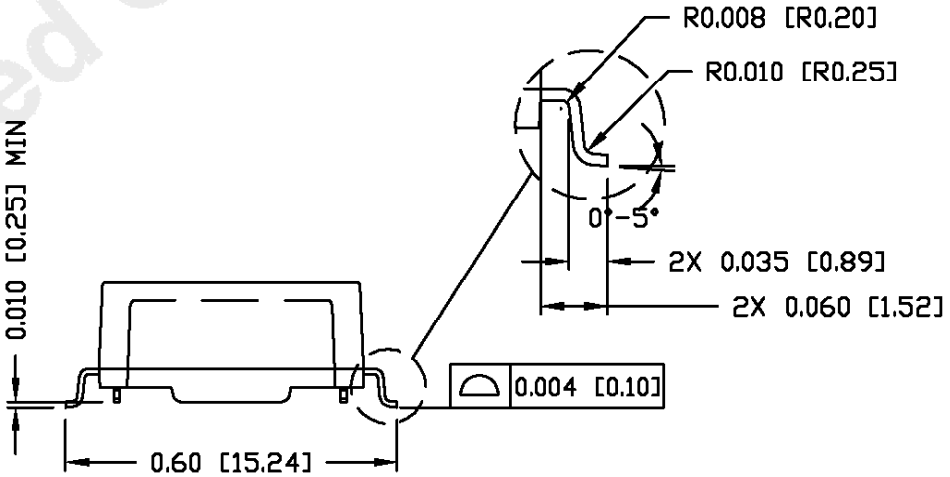
RoHS



SUGGESTED PCB PAD LAYOUT







NOTES:

1. STANDARD MARKING REFER TO DOC. HAND-WORK-04.
2. PACKAGE CODE: "RQS001".

REV. : A | PAGE : 3

ORIGINATED BY	DATE	TITLE	PART NO. / DRAWING NO.	STANDARD DIM. TOL. IN INCH		[ ] METRIC DIM. AS REF.
Lawrence Tsang	2020-06-05	MECHANICAL OUTLINE	S558-5500-JB	.X		UNIT : INCH [mm]
DRAWN BY	DATE		FILE NAME	.XX	±0.01	SCALE : N/A
ZC Guo	2020-06-05		S5585500JBA.DWGC	.XXX	±0.005	SIZE : A4

POWER | PROTECT | CONNECT