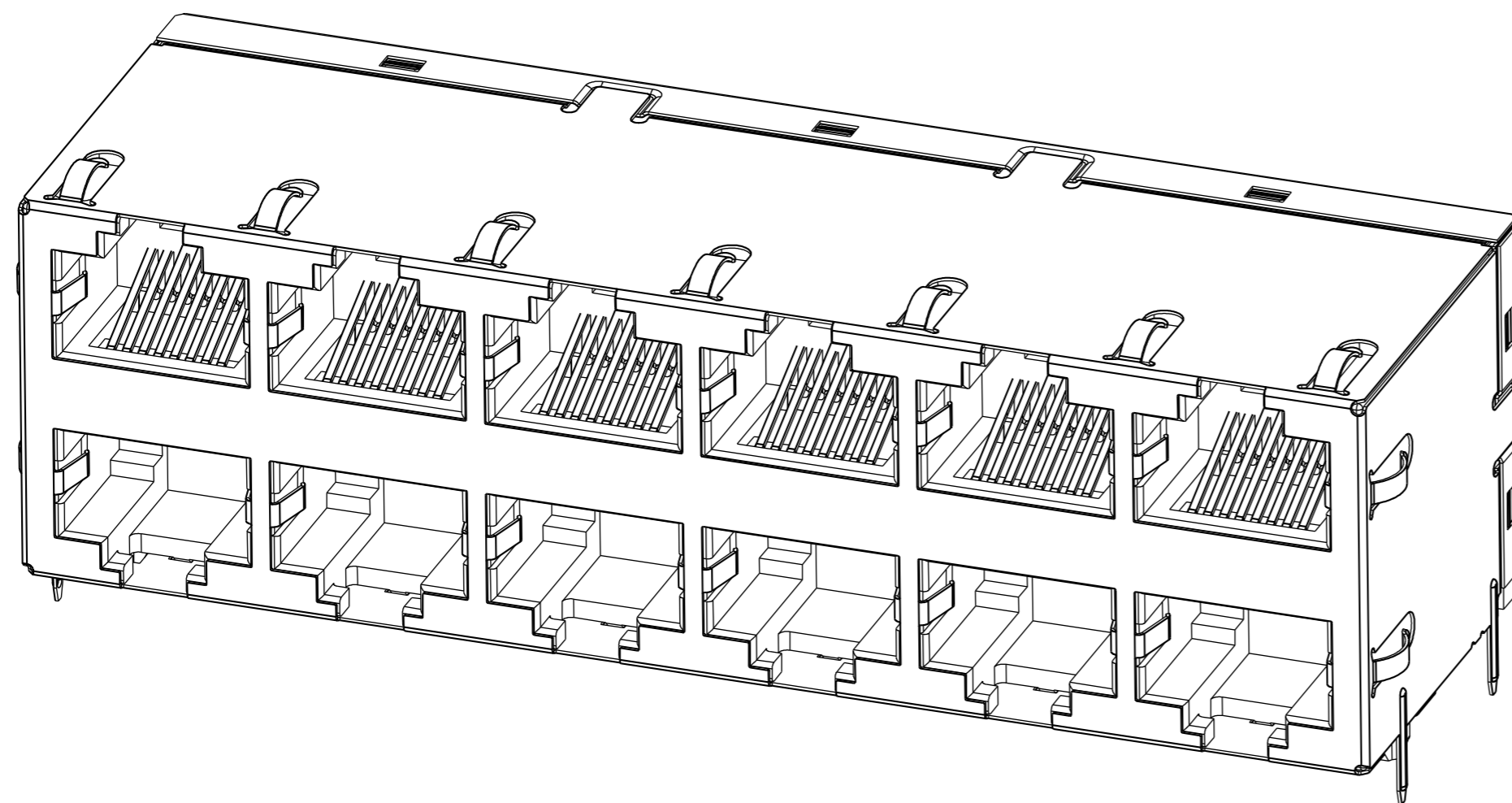


THIS DRAWING AND THE SUBJECT MATTER SHOWN THEREON ARE CONFIDENTIAL AND THE PROPERTY OF BEL/STEWART/TRP CONNECTOR AND SHALL NOT BE REPRODUCED, COPIED, OR USED IN ANY MANNER WITHOUT THE WRITTEN CONSENT OF TRP CONNECTOR.

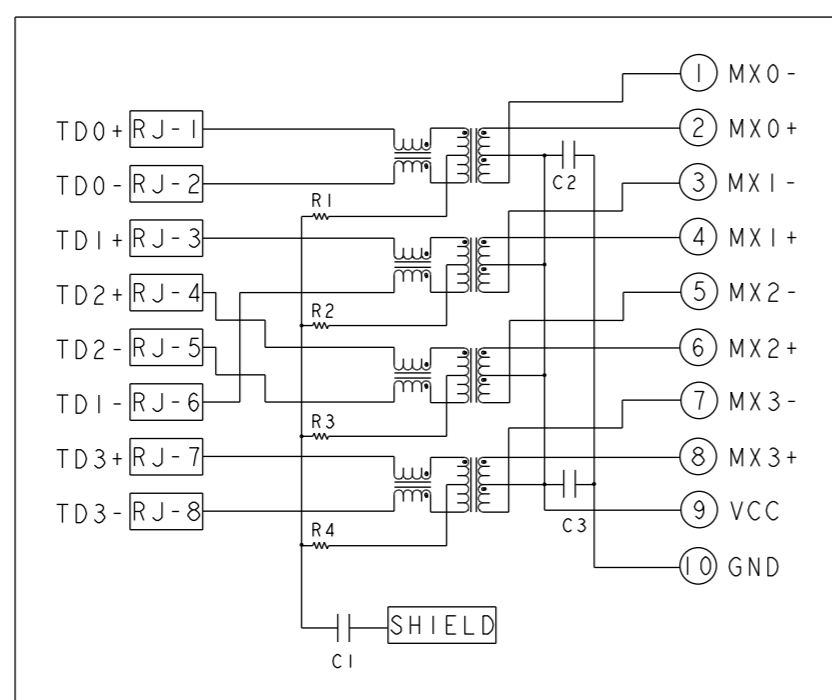
PRODUCT MAY BE PROTECTED BY ONE OR MORE OF THE FOLLOWING US PATENTS:
 5736910 5939955 6425781 6428361 6554638 6840817 7123117
 7429195 7717749 7808751 6217391 6149050 7924130

REVISIONS				
REV	DATE	DESCRIPTION	BY	APPV
D	04JAN2015	COMPANY LOGO CHANGE	RL	TY



S8G89 GIGABIT CIRCUIT

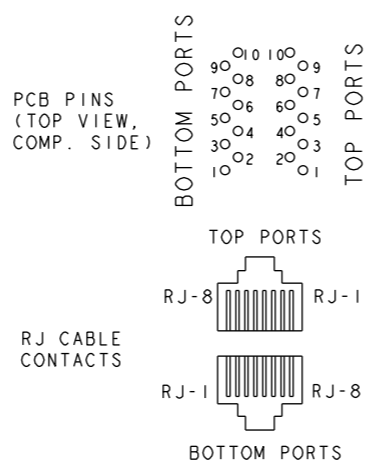
TOP AND BOTTOM PORTS
 (REFER TO PIN DESIGNATIONS BELOW)



C1 = 1000pF, 2kV, DECOUPLING CAPACITOR
 C2 - C3 = 0.01uF, 50V, CAPACITORS
 R1-R4 = 75 Ohms, RESISTORS

PIN DESIGNATIONS

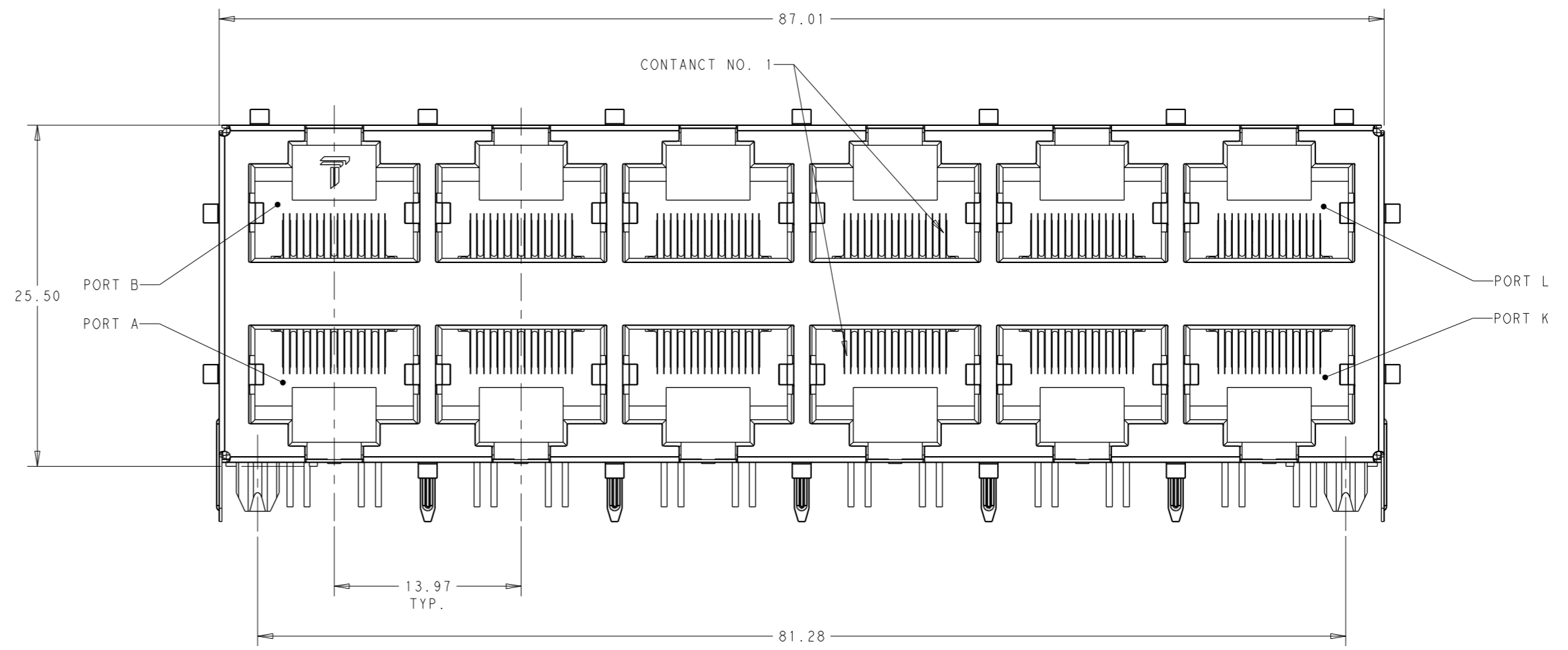
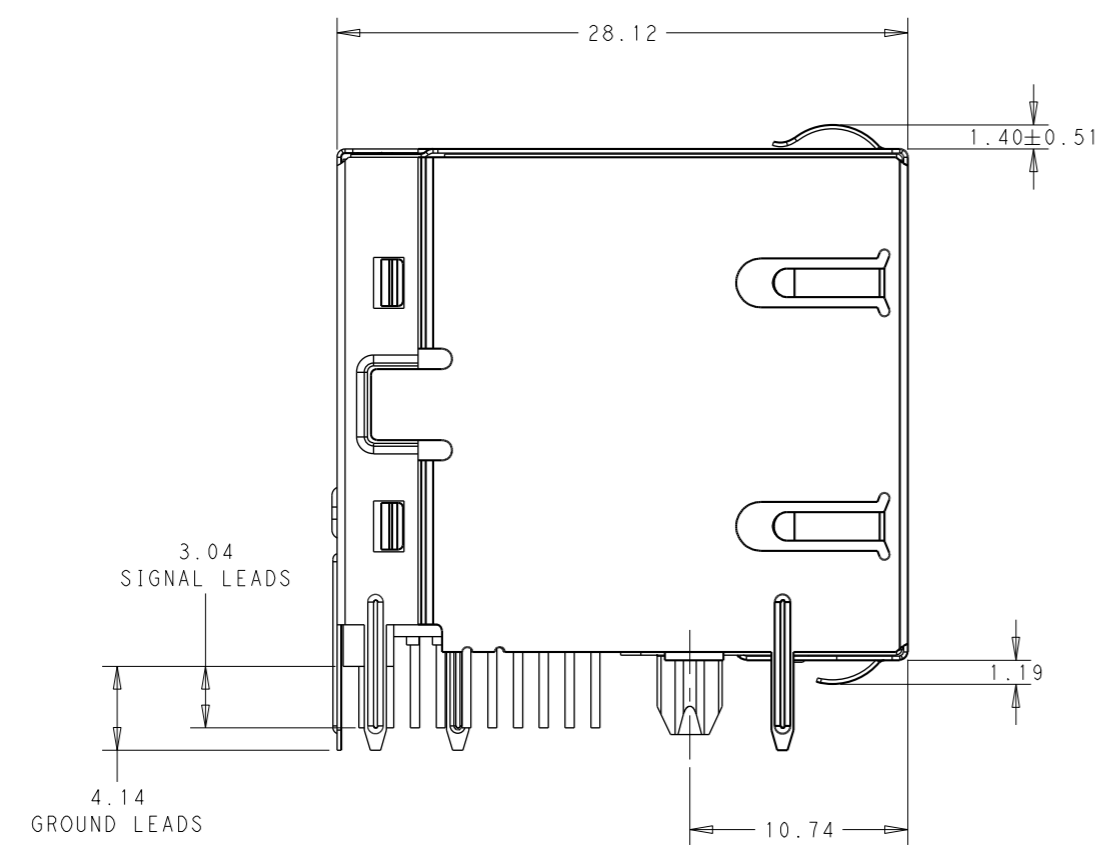
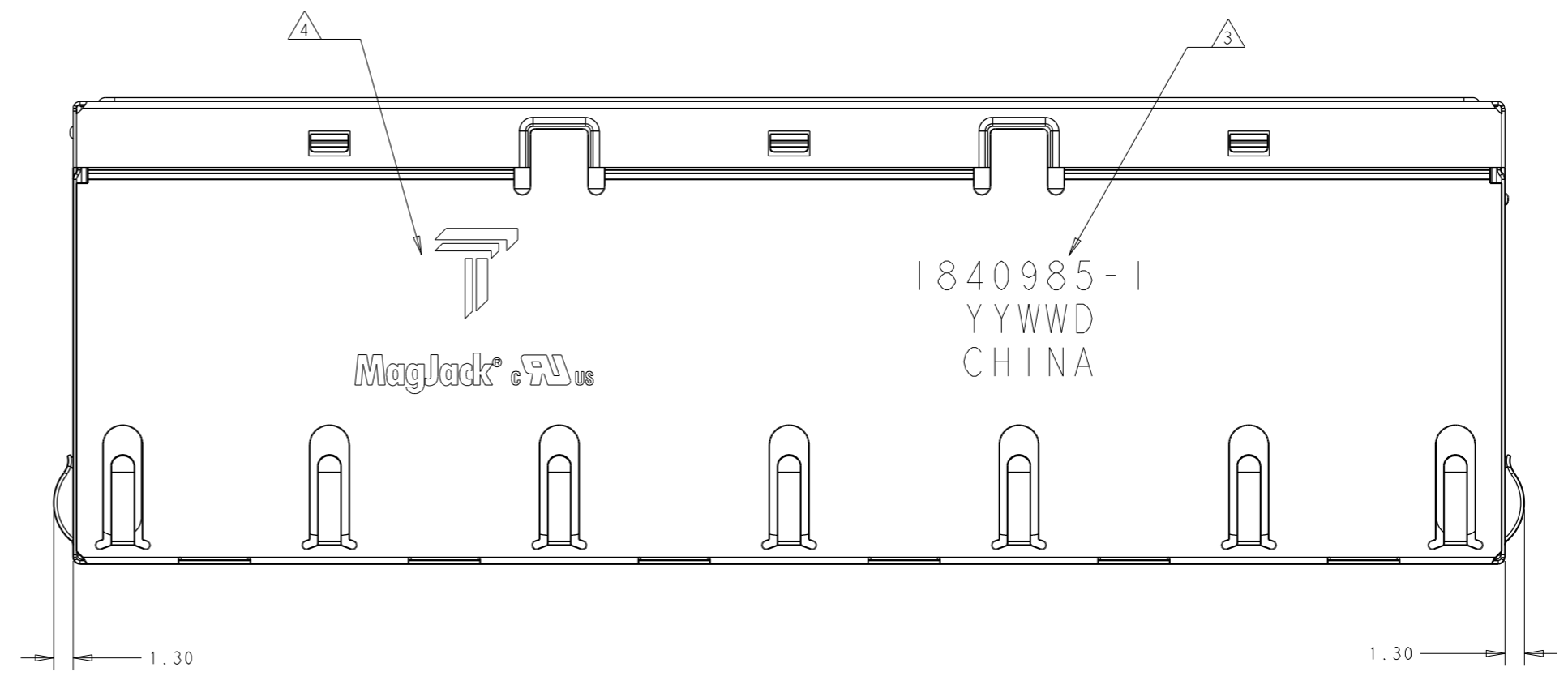
(REPEAT FOR VERTICAL PAIR OF PORTS)




1. MATERIALS:
 PLASTIC HOUSING: BLACK, THERMOPLASTIC FLAMMABILITY RATING UL 94V-0
 SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL,
 POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS,
 CONTACTS: PHOSPHOR BRONZE, 1.27um MIN OVERALL NICKEL
 UNDERPLATE WITH SELECT 1.27um MIN GOLD AT MATING INTERFACE
 AND 2.54um MIN MATTE TIN ON SOLDER TAILS.
 ALL PC BOARDS: HIGH TEMPERATURE PCB, TG>170°C
2. MAGNETICS
 APPLICATION: 10/100/1000 BASE-T
 IMPEDANCE: 100 OHMS
 TURNS RATIO (CHIP:CABLE): 1:1 ALL FOUR PAIRS
 OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS,
 8mADC BIAS FROM 0°C TO 70°C, ALL FOUR PAIRS
 ALL FOUR PAIRS BI-DIRECTIONAL
 PERFORMANCE @ 25°C:
 INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
 RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
 12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz
 CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
 33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
 COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
 ISOLATION VOLTAGE: 2250VDC(MAX) FOR 60 SECONDS WITH A RISE TIME OF
 500V/SEC AND WITH ALL PORTS CONNECTED.
3. PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE LOCATED IN APPROXIMATE AREA SHOWN. DATE CODE: YYWW WHERE "YY" IS YEAR, "WW" IS WEEK, "D" IS DAY OF WEEK, WITH SUNDAY=1.
4. TRP LOGO AND AGENCY APPROVAL LOGO ARE LOCATED IN APPROXIMATE AREA SHOWN.
5. OPERATING TEMP: FROM 0°C TO +70°C.
6. RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.
7. INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDIX.
8. DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.
9. BASIC DIMENSION ESTABLISHED BY CUSTOMER, BUT MAY NOT BE GREATER THAN 5.08mm.
10. THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 260°C MAX, 10 SECONDS MAX.

1840985-1
 PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.		DNW: TOM HSU/SEAN HE 17SEP2010	 trp CONNECTOR a bel group DONGGUAN CHINA
DIMENSIONS: mm		CHK: TONY YUAN 17SEP2010	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APV: KEITH ZHU 17SEP2010	
0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ± 4 PLC ± ANGLES ± APPLICATION SPEC	MODEL NAME MAGJACK STACK NON-PoE	DESC 2X6 S8G89 GIGABIT CIRCUIT	SIZE: A1 CAGE CODE: 1840985 DRAWING NO: 1840985
PRODUCT SPEC 108-104004	CUSTOMER DRAWING	SCALE: NTS SHEET: 1 of 3 REV: D	RESTRICTED TO:



THIS DRAWING IS A CONTROLLED DOCUMENT.		DNW TOM HSU/SEAN HE 17SEP2010	 TRP CONNECTOR a bel group DONGGUAN CHINA
DIMENSIONS: mm		CHK TONY YUAN 17SEP2010	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APPV KEITH ZHU 17SEP2010	
0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ± 4 PLC ± ANGLES ± APPLICATION SPEC -	MODEL NAME MAGJACK STACK NON-POE	DESC 2X6 S8689 GIGABIT CIRCUIT	RESTRICTED TO -
PRODUCT SPEC 108-104004	SCALE NTS	SIZE A1 CAGE CODE DRAWING NO. 1840985	SHEET 2 OF 3 REV D

