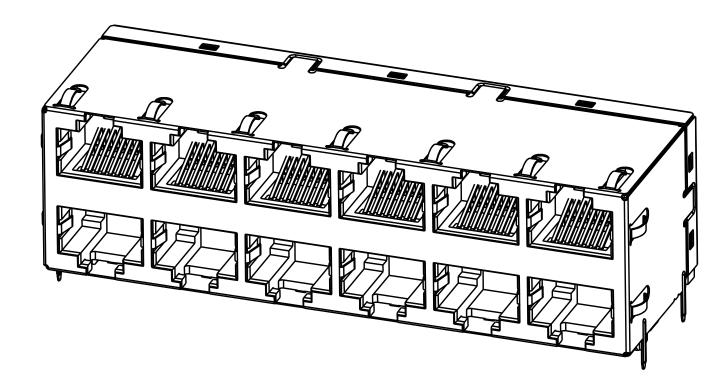
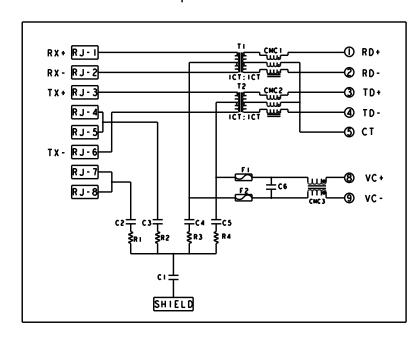
H EC-1411035 COMPANY LOGO CHANGE 20JAN2015 CZ TY

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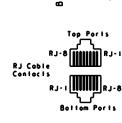


S8V28 10/100 PoE CIRCUIT

Top and Bottom Ports



Pin Designations (Repeat for each vertical pair of ports.) PCB Pins



CI = 1000pF, 2KV, ±10%, X7R DECOUPLING CAP C2 - C5 = 22nF, 100V, $\pm 10\%$ CAPACITORS RI-R4 = 75 Ohms, 1/16 W, $\pm 5\%$ RESISTORS C6 = 0.1uF,100VFI-F2=125V, IA

MATERIALS: PLASTIC HOUSING: BLACK, THERMOPLASTIC FLAMMABILITY RATING UL 94V-0 SHIELD: BRASS, PREPLATED WITH 0.76 um MIN SEMI-BRIGHT NICKEL, POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS, CONTACTS: PHOSPHOR BRONZE, 1.27um MIN OVERALL NICKEL UNDERPLATE WITH SELECT 1.27um MIN GOLD AT MATING INTERFACE AND 2.54um MIN MATTE TIN ON SOLDER TAILS.
ALL PC BOARDS: HIGH TEMPERATURE PCB,TG>170°C

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MAGNETICS APPLICATION: 10/100 BASE-T, PoE IMPEDANCE: 100 OHMS IMPEDANCE: 100 OHMS
TURNS RATIO (CHIP:CABLE): TX = 1:1, RX = 1:1
OPEN CIRCUIT INDUCTANCE (OCL): 350uh MIN @100khz, 0.1VRMS,
8mADC BIAS FROM 0°C TO 70°C, TX AND RX
POE CURRENT: 350mADC MAX
PERFORMANCE @ 25°C:
INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 30MHz
RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 30MHz 18-20LOG(f/30)dB MIN FROM 30.1MHz TO 60MHz 18-20LOG(1730) dB MIN FROM 30.1MH2 10 60MH2
12dB MIN FROM 60.1MH2 TO 80MH2
CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MH2 TO 40MH2
33-20LOG(1750) dB MIN FROM 40.1MH2 TO 100MH2
COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MH2 TO 100MH2
1SOLATION VOLTAGE: 2250VDC(MAX) FOR 60 SECONDS WITH A RISE TIME OF 500V/SEC AND WITH ALL PORTS CONNECTED.

A PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE LOCATED IN APPROXIMATE AREA SHOWN.DATE CODE: YYWWD "YY" IS YEAR, "WW" IS WORK WEEK, "D" IS DAY OF WEEK, WITH SUNDAY=I

TRP LOGO AND AGENCY APPROVAL LOGO ARE LOCATED IN APPROXIMATE AREA SHOWN.

5. OPERATING TEMP: FROM 0°C TO +70°C.

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6. RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.

⚠ INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDIX.

⚠ DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.

⚠ BASIC DIMENSION ESTABLISHED BY CUSTOMER, BUT MAY NOT BE GREATER THAN 5.08mm.

10. THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 260°C MAX, 10 SECONDS MAX.

SKY ZHAO 05NOV20 TOMMY REN 05NOV200 2X6 S8V28 10/100 PoE W/O LED $\oplus \ominus$ MAGJACK STACK POE C-1840900 108-104004 TOMER DRAWING

PART NUMBER

1840900-

