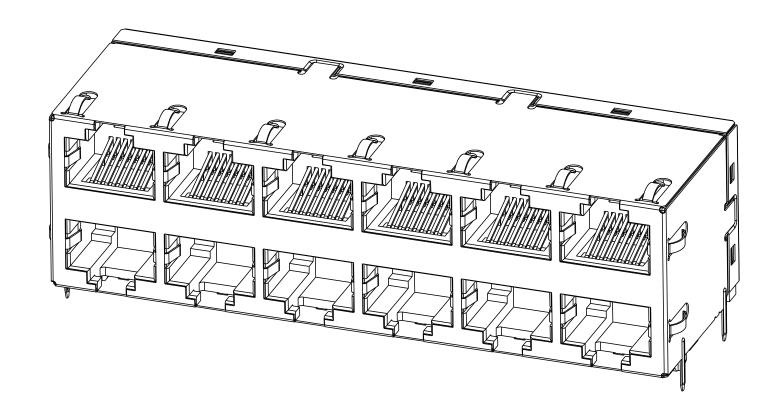
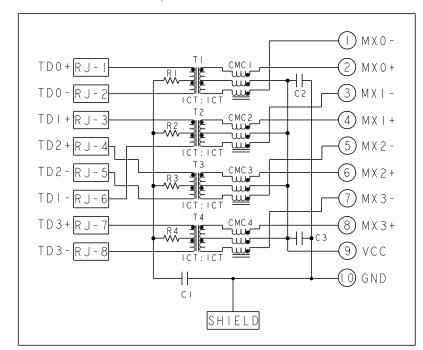
THIS DRAWING AND THE SUBJECT MATTER SHOWN THEREON ARE CONFIDENTIAL AND THE PROPERTY OF BEL/STEWART/TRP CONNECTOR AND SHALL NOT BE REPRODUCED, COPIED, OR USED IN ANY MANNER WITHOUT THE WRITTEN CONSENT OF TRP CONNECTOR. PRODUCT MAY BE PROTECTED BY ONE OR MORE OF THE FOLLOWING US PATENTS: 5736910 5939955 6425781 6428361 6554638 6840817 7123117 7429195 7717749 7808751 6217391 6149050 7924130

DESCRIPTION H EC-1411035 COMPANY LOGO CHANGE 03MAR2015 TR TY

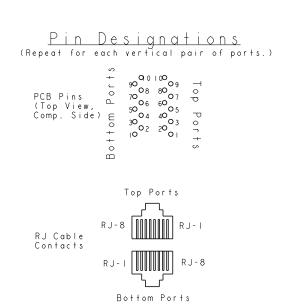


S8G56 GIGABIT CIRCUITAA

Top and Bottom Ports



CI = 1000pF, 2kV Capacitor C2 - C3 = 10nF, 50V Capacitors RI-R4 = 75 Ohms, 1/16 W, Resistors



MATERIALS:
PLASTIC HOUSING: BLACK, THERMOPLASTIC FLAMMABILITY RATING UL 94V-0
SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL,
POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS,
CONTACTS: PHOSPHOR BRONZE, I.27um MIN OVERALL NICKEL
UNDERPLATE WITH SELECT I.27um MIN GOLD AT MATING INTERFACE
AND 2.54um MIN MATTE TIN ON SOLDER TAILS. ALL PC BOARDS: HIGH TEMPERATURE PCB, TG>170°C

△ MAGNETICS APPLICATION: 10/100/1000 BASE-T IMPEDANCE: 100 OHMS TURNS RATIO (CHIP:CABLE): I:I ALL FOUR PAIRS

OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.IVRMS,

8mADC BIAS FROM 0°C TO 70°C, ALL FOUR PAIRS

ALL FOUR PAIRS BI-DIRECTIONAL PERFORMANCE @ 25°C: INSERTION LOSS (IL): I.IdB MAX FROM 0.5MHz TO 100MHz
RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz CROSSTALK ATTENDATION: 358B MIN FROM 0.5MHz TO 40MHz
33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
ISOLATION VOLTAGE: 2250VDC(MAX) FOR 60 SECONDS WITH A RISE TIME OF
500V/SEC AND WITH ALL PORTS CONNECTED.

PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE LOCATED IN APPROXIMATE AREA SHOWN.DATE CODE: YYWWD "YY" IS YEAR, "WW" IS WORK WEEK, "D" IS DAY OF WEEK, WITH SUNDAY=I

4 TRP CONNECTOR LOGO AND AGENCY APPROVAL LOGO ARE LOCATED IN APPROXIMATE AREA SHOWN.

5. OPERATING TEMP: FROM 0°C TO +70°C.

6. RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.

1 INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDIX.

DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.

BASIC DIMENSION ESTABLISHED BY CUSTOMER, BUT MAY NOT BE GREATER THAN 5.08mm.

IO. THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 260°C MAX, IO SECONDS MAX.

270CT2009	A. Luce					
		SIDE EMI FINGER			DIM A	PART NUMBER
		SEE	SHEET	3	2.84	1840371-1
		SEE	SHEET	3	3.43	1840371-2
	Γ	SEE	SHEET	2	2.84	2-1840371-1
		SEE	SHEET	2	2.06	2-1840371-3

TONY YUAN 270CT2009 2X6 S8G56 GIGABIT W/O LED $\bigoplus \bigoplus$ MAGJACK STACK Non-PoE RODUCT SPE C-1840371 108-104004 CUSTOMER DRAWING

TOMMY REN 270CT2009 HIS DRAWING IS A CONTROLLED DOCUMENT

